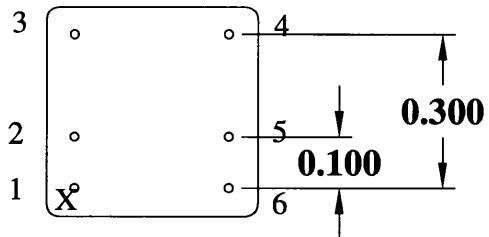
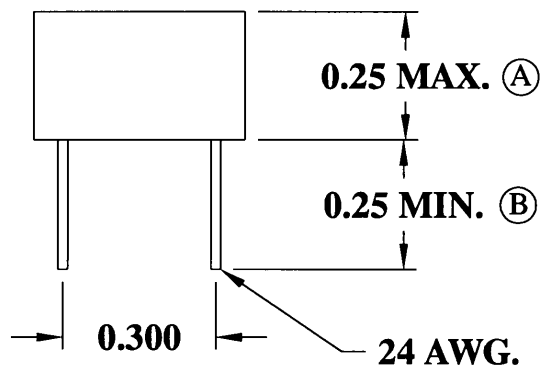
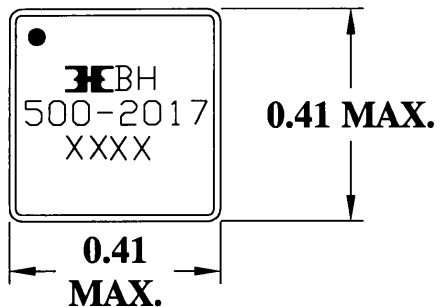
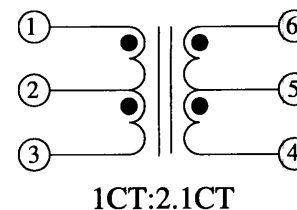


MECHANICAL



NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 1.

SCHEMATIC



ELECTRICAL SPECIFICATIONS


URNS RATIO: 1CT:2.1CT

OPEN CIRCUIT INDUCTANCE: 400uH MIN. (1-3) 100 KHz 20mV

DC RESISTANCE: (1-3) 1 OHMS MAX. (6-4) 1.7 OHMS MAX.

HIGH VOLTAGE TEST: 1500 VRMS FOR 1 SEC.

ET CONSTANT: 18.3 Vu SEC. MIN.

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				DRAWN: K. LOFTUS	
				ENGR: B. MARKS	TITLE:
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	ISOLATION TRANSFORMER
					PART NO.
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	500-2017