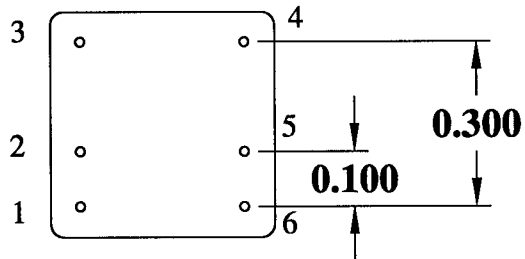
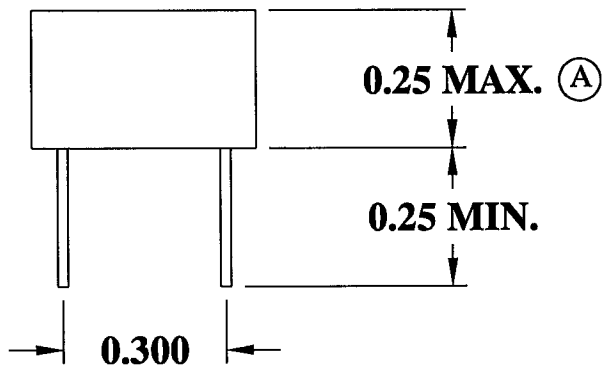
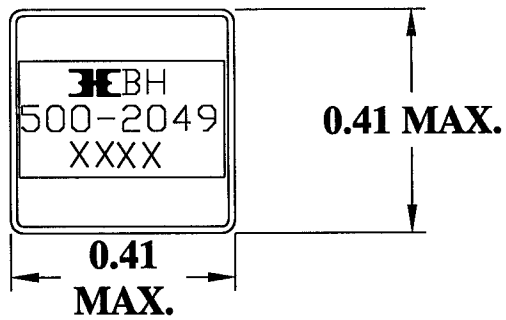
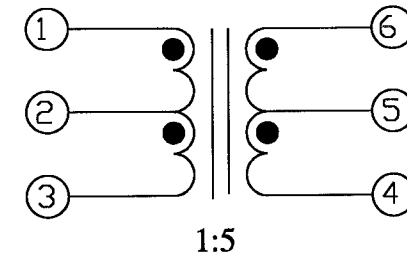


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1CT:5CT


OPEN CIRCUIT INDUCTANCE: 400uH MIN. (1-3) 100 KHz 30mV

PRIMARY LEAKAGE INDUCTANCE: 0.2uH NOM.

INTERWINDING CAPACITANCE: 40pF NOM.

HIGH VOLTAGE TEST: 500 VAC/ 1 SEC.

ET CONSTANT: 18.3Vu SEC. MIN.

| | | | | | |
|-------|----------|----------------|-----------|--|---|
| | | | | DESIGN PER: |  BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics |
| | | | | CATALOG | |
| | | | | DRAWN: K. LOFTUS | |
| | | | | ENGR: B. MARKS | |
| B | 12-15-00 | UPDATE TO CADD | <i>BM</i> | UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 | TITLE: TRANSFORMER |
| A | 6-29-95 | CUSTOMER PRINT | BM | | PART NO. 500-2049 |
| ISSUE | DATE | REVISION | BY | | PAGE 1 OF 1 |