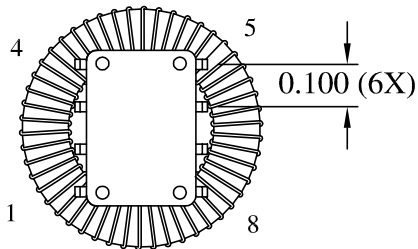
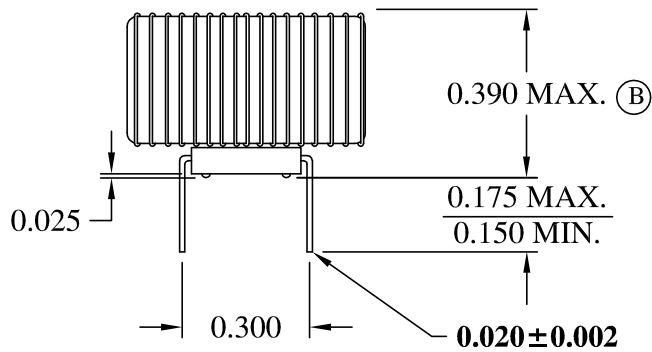
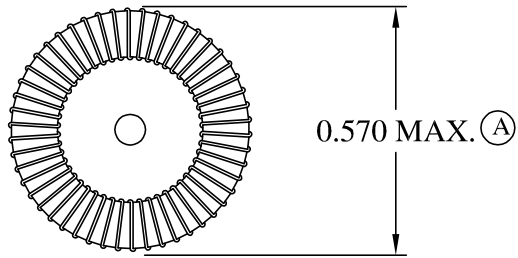
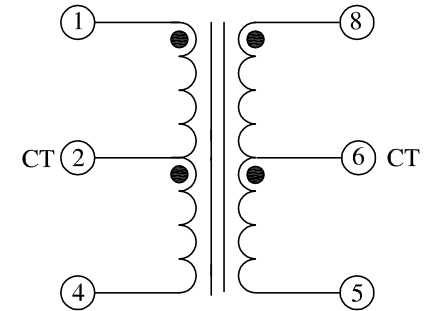


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 430mH MIN. 10 KHz @ 300mV (1-4)


PRIMARY LEAKAGE INDUCTANCE: 200uH MAX.

INTERWINDING CAPACITANCE: 750pF MAX.

DC RESISTANCE: 13.2 OHMS MAX. (1-4)

ET CONSTANT: 900 Vu SEC. MIN.

HIGH VOLTAGE TEST: 2900 VDC

				DESIGN PER:	 <b>BH</b> ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<b>BH CATALOG</b>	
F	2-13-12	NOW BH CATALOG	<i>lsm</i>	DRAWN: K. LOFTUS	TITLE: <b>TRANSFORMER</b>
E	2-13-01	UPDATE TO CADD	BM	ENGR: B. MARKS	
D	12-10-92	ECR #1503	BM	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 1 OF 1	PART NO. <b>500-1542</b>
C	4-17-90	DCR FROM 12 TO 13	JDC		
B	7-18-89	Q5640 NOW BH P/N 500-1542	JDC		
A	3-26-87	CUSTOMER PRINT	BM		
ISSUE	DATE	REVISION	BY		