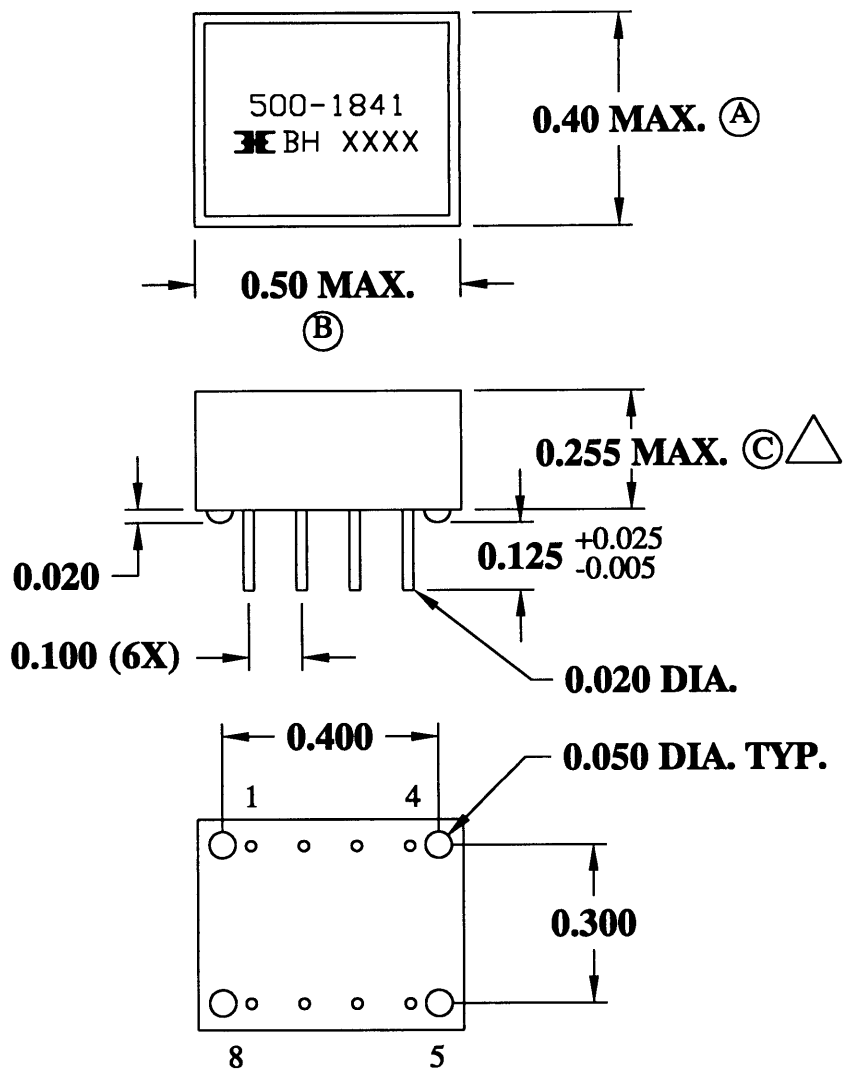
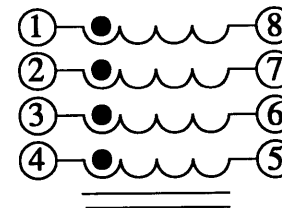


**MECHANICAL**



**SCHEMATIC**



**ELECTRICAL SPECIFICATIONS**

URNS RATIO: 1:1:1:1

PRIMARY INDUCTANCE: 8uH MIN. (1-8) 100 KHz 20mV



PRIMARY LEAKAGE INDUCTANCE: 0.2uH MAX. (1-8 SHORT 4-5)  
120 KHz 50mV

PRIMARY CAPACITANCE: 10pF MAX. (1-5) 120 KHz 50mV

DC RESISTANCE: 0.2 OHMS MAX.

ET CONSTANT: 2.1 Vu SEC. MIN.

HIGH VOLTAGE TEST: 1000 VDC (1-2, 3, 4)  
(2-3, 4) (3-4)

				DESIGN PER:	 <b>BH ELECTRONICS, INC.</b> BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
G	10-23-00	LINES FOR HEIGHT DIM.		<b>CATALOG</b>	
F	9-29-00	UPDATE TO CADD	JDC	DRAWN: K. LOFTUS	<b>TITLE: COMMON MODE CHOKE</b> <b>PART NO. 500-1841</b>
E	10-15-99	ECR #4445 CHG. HT. DIM.	JDC	ENGR: J. DECRAMER	
D	4-17-96	ECR #2728 REMOVE CENTER LINE TOL	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 1 OF 1	
C	2-12-96	CHANGE TO PRODUCTION	JDC		
B	12-19-95	ECR #2518 ADD TOL AND TEST DATA			
A	1-9-92	CUSTOMER PRINT			
<b>ISSUE</b>	<b>DATE</b>	<b>REVISION</b>	<b>BY</b>		