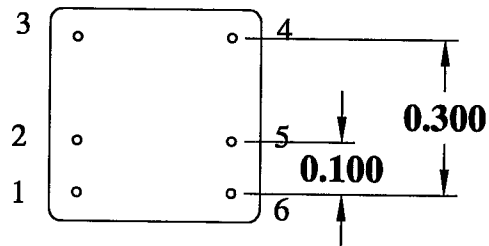
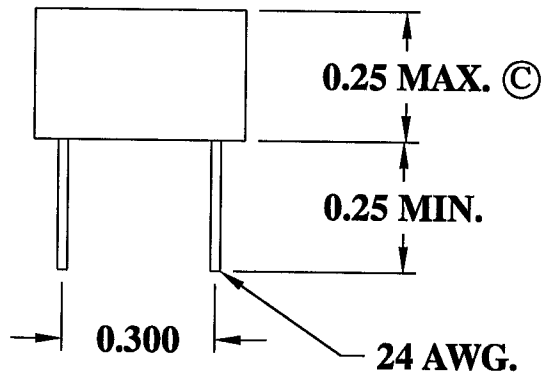
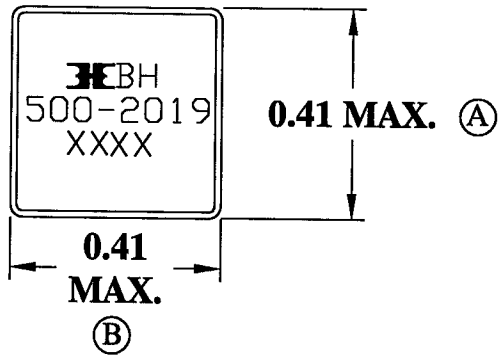
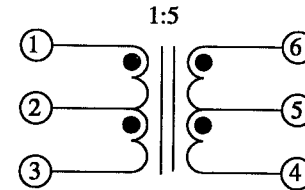


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS


URNS RATIO: 1CT:5CT

OPEN CIRCUIT INDUCTANCE: 400uH MIN. (1-3) 100 KHz 20mV

DC RESISTANCE: (1-3) 1 OHMS MAX. (6-4) 4.0 OHMS MAX.

HIGH VOLTAGE TEST: 1500 VRMS FOR 1 SEC.

ET CONSTANT: 18.3 Vu SEC.

ISSUE	DATE	REVISION	BY	DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG +5V TO +24V	
D	10-6-00	ECR #4831 UPDATE TO CAD	<i>KLO</i>	DRAWN: K. LOFTUS	TITLE: <b>ISOLATION TRANSFORMER</b> PART NO. <b>500-2019</b>
C	2-23-00	ADD DIMENSION TARGETS	BM	ENGR: B. MARKS	
B	5-8-95	REMOVE STANDOFFS CORRECT DCR	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	
A	4-26-94	CUSTOMER PRINT	BM		
				PAGE 1 OF 1	