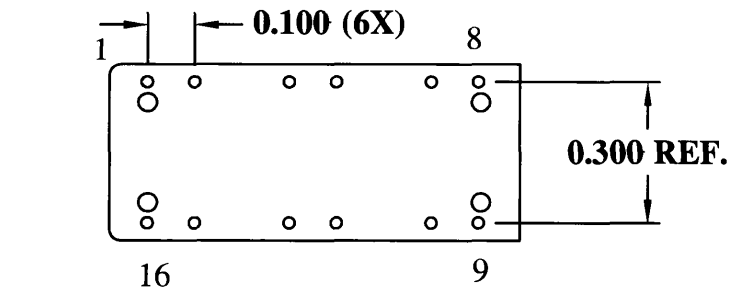
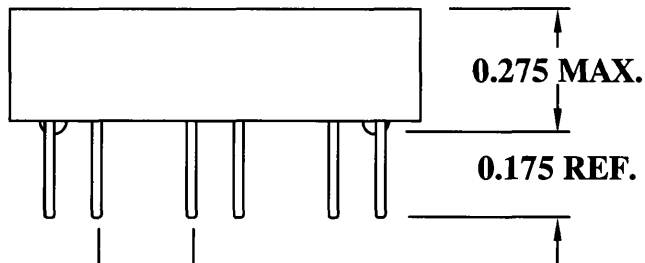
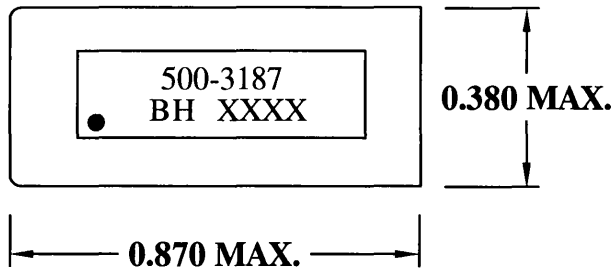
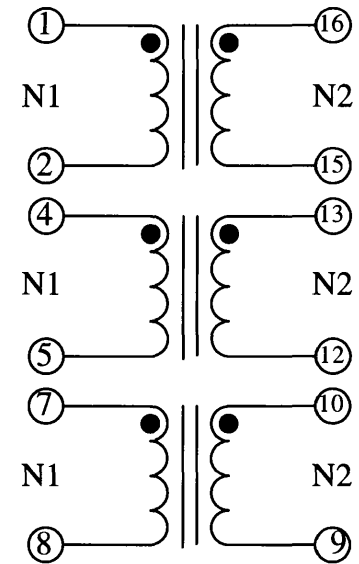


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 80-120μH @ 0.020 VRMS, 100 KHz (1-2) (4-5) (7-8)

DC RESISTANCE: 0.30 OHMS MAX. (1-2) (4-5) (7-8) (16-15) (13-12) (10-9)

HIGH VOLTAGE TEST: 500 VRMS / 1 SEC.


PRIMARY LEAKAGE INDUCTANCE: 0.2uH MAX. (1-2) (4-5) (7-8) 120 KHz 50mV

INTERWINDING CAPACITANCE: 10pF MAX. (1-16) (4-13) (7-10) 120 KHz 50mV

PRIMARY ET CONSTANT: 2.1 Vu SEC. MIN.

PULSE INDUCTANCE: 80uH MIN.

TEMPERATURE CLASS: 200°C

				DESIGN PER:			
				200C ETHERNET TRANSFORMER		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics	
				CUSTOM	NON-PROPRIETARY		X
				DRAWN:	K. LOFTUS		
				ENGR:	J. DECRAMER		
C	11-11-11	REVISED TITLE BLOCK		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:	TRANSFORMER
B	10-18-11	Q12915 NOW 500-3187				PART NO.	500-3187
A	9-30-11	CUSTOMER PRINT	JDC				
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1			