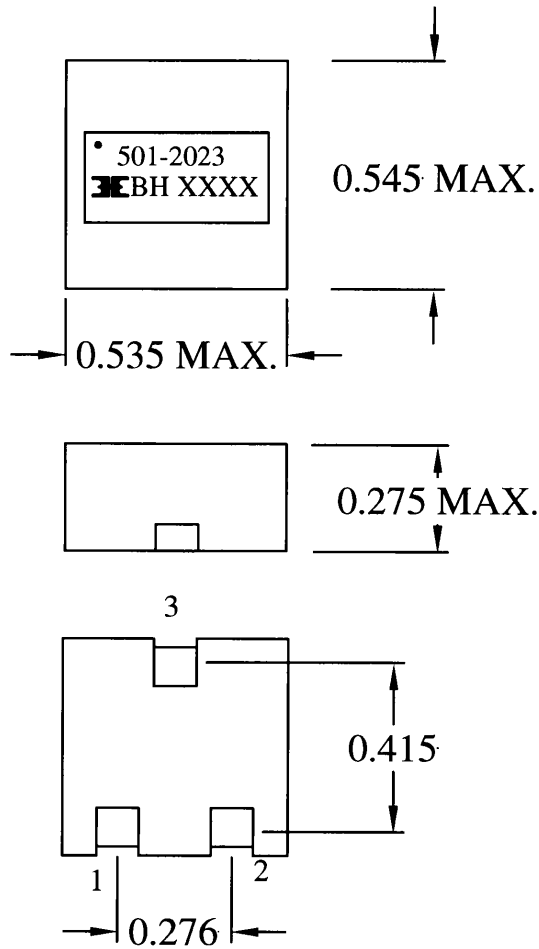
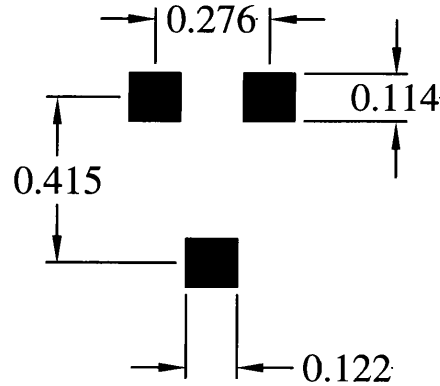


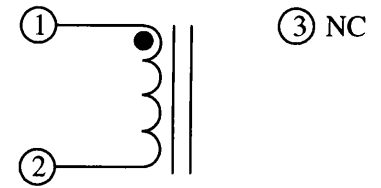
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS


OPEN CIRCUIT INDUCTANCE: 4.0 μ H \pm 20%

DC RESISTANCE: 0.0080 OHMS MAX.

MAX. I PEAK: 10.3A (NOTE 2)

NOTE:

1. INDUCTANCE TESTED AT 100 KHz 500mV, 0 ADC
2. AVERAGE CURRENT FOR A 40°C TEMP. RISE ABOVE 25°C AMBIENT.

ISSUE	DATE	REVISION	BY	DESIGN PER:	TITLE:
				CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
F	3-19-12	NOW INDIVIDUAL PRINTS	JDC	<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
E	3-5-12	DIMENSION CHANGE	JDC	DRAWN: K. LOFTUS	INDUCTOR
D	12-23-08	REMOVE NOTE 2, RENUMBER	JDC	ENGR: J. DECRAMER	
C	5-23-08	UPDATE TO CATALOG	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005	PART NO. 501-2023
B	3-15-05	Q11641-X NOW 501-20XX			
A	2-21-05	CUSTOMER PRINT	NGD	PAGE 1 OF 1	