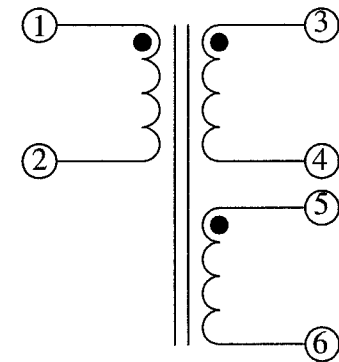
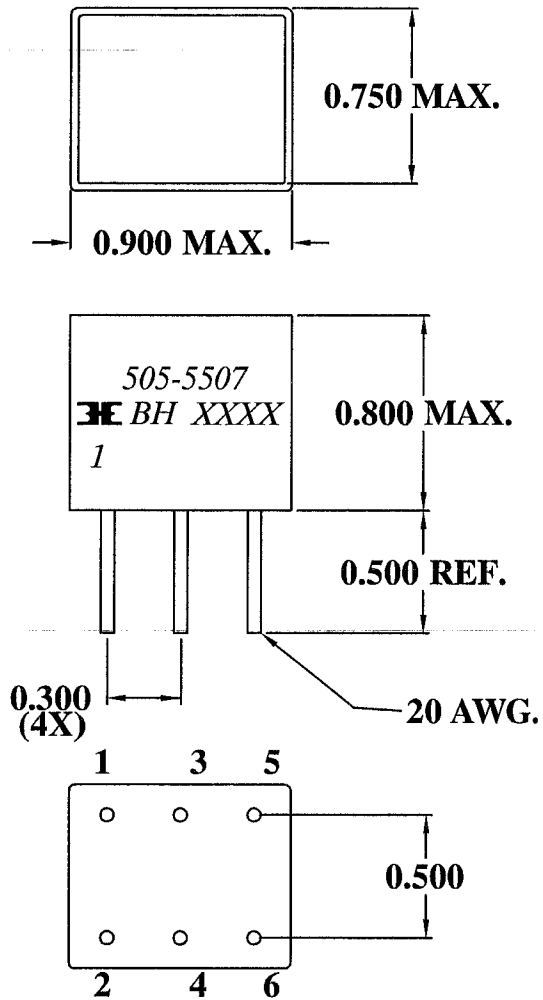


MECHANICAL

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1:1

OPEN CIRCUIT INDUCTANCE: 20mH MIN. (1-2) 1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 200μH MAX.


(1-2 SH 3-4, 5-6)

INTERWINDING CAPACITANCE: 50pF MAX.

(1, 2 - 3, 4) (1, 2 - 5, 6)

HIGH VOLTAGE TEST: 2500 VRMS / 1 SEC.

ET CONSTANT: 360 V μSEC. MIN.

				DESIGN PER: <b>BH CATALOG</b>	 <b>BH ELECTRONICS, INC.</b> BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM      NON-PROPRIETARY <input checked="" type="checkbox"/>	
C	11-7-05	ECR #6633 UPDATE TO CADD	<i>km</i>	DRAWN: K. LOFTUS	TITLE: <b>SCR TRIGGER TRANSFORMER</b> PART NO. <b>505-5507</b>
B	7-29-96	ECR #2830 CHG. LOC TO	JDC	ENGR: B. MARKS	
		1 KHz 100mV		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	
A	7-10-96	CUSTOMER PRINT	JDC	PAGE 1 OF 1	
ISSUE	DATE	REVISION	BY		