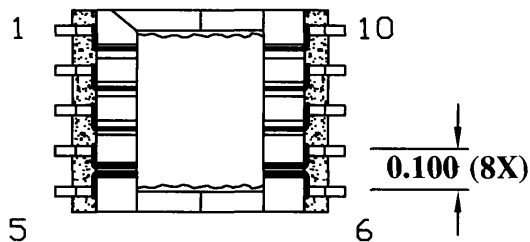
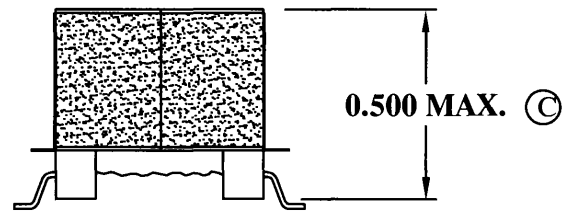
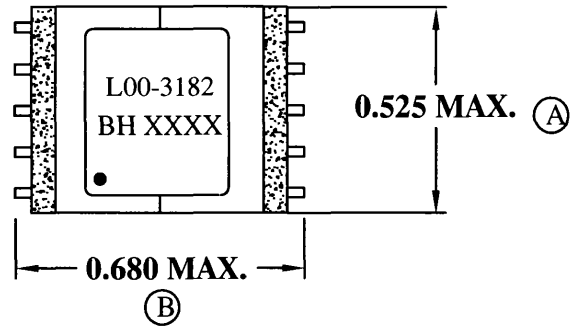
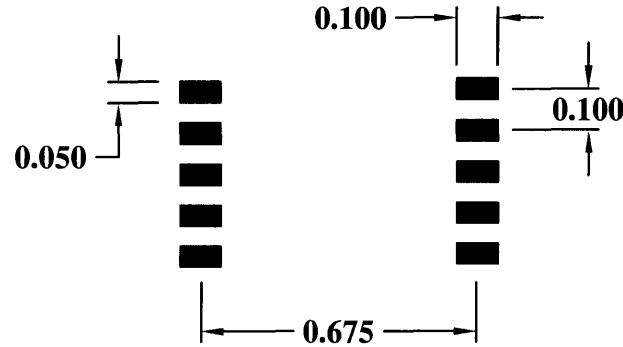


MECHANICAL

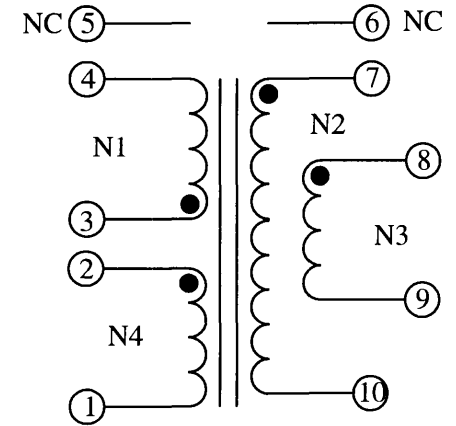


COPLANARITY 0.004 MAX.

SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 4:1:1:1

OPEN CIRCUIT INDUCTANCE: 300μH ±10% (3-4) 100 KHz 100mV

INDUCTANCE @ DC BIAS: 240μH NOM. @ 0.8A

PRIMARY LEAKAGE INDUCTANCE: 4μH NOM. (3-4 SH 7, 8, 9, 10)
120 KHz 50mV


DC RESISTANCE: 0.31 OHMS NOM. (3-4)

0.15 OHMS NOM. (7-10) (8-9)

1.10 OHMS NOM. (8-2)

HIGH VOLTAGE TEST: 4000 VAC / 1 SEC. 5 SEC. RAMP-UP
(2-7, 8) (3-7, 8)

RoHS

				DESIGN PER:	BH CATALOG		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM	<input checked="" type="checkbox"/> NON-PROPRIETARY		
				DRAWN:	K. LOFTUS		TITLE: TRANSFORMER
				ENGR:	M. DVORAK		
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005			
C	8-19-11	REVISE DESIGN					PART NO. L00-3182
B	8-9-11	Q12848-1 NOW L00-3182	MD				
A	4-28-11	CUSTOMER PRINT	MD				
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1			