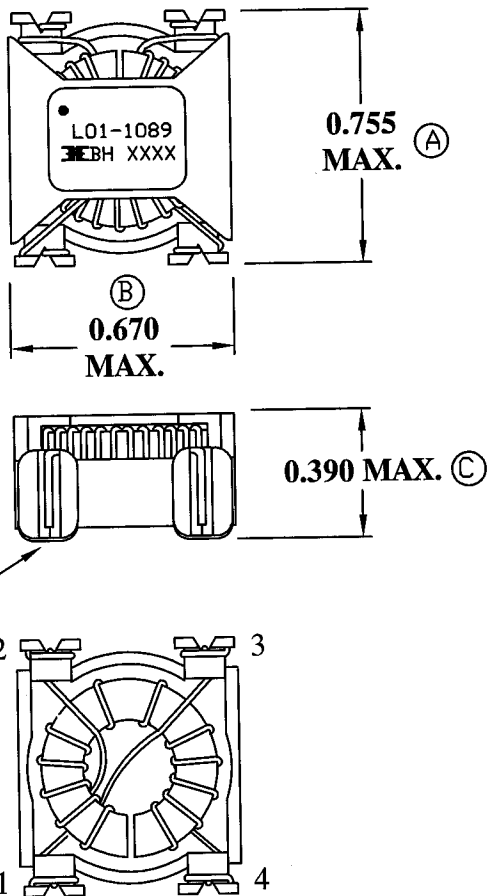
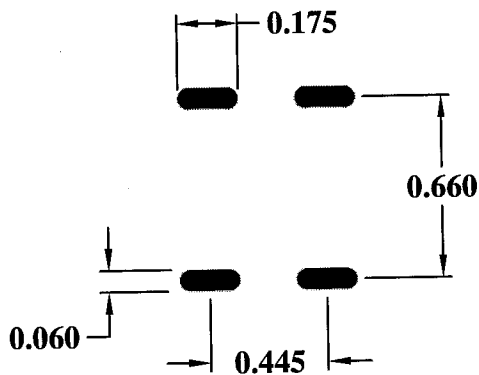


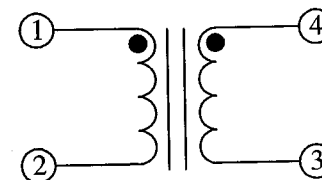
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1


OPEN CIRCUIT INDUCTANCE: 4.23 μ H \pm 10% @ 0 ADC (1-2)
10 KHz 100mV

3.1 μ H TYP. @ 9.5 ADC (1-2) 10 KHz 100mV

DC RESISTANCE: 0.0089 OHMS NOM. (1-2) (3-4)

HIGH VOLTAGE TEST: 500 VRMS (1-3)

RoHS Compliant

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN: K. LOFTUS	TITLE: INDUCTOR
				ENGR: J. DECRAMER	
B	11-7-05	ECR #6634 INC. WIDTH SPEC.	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005 PAGE 1 OF 1	PART NO. L01-1089
A	9-20-05	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY		