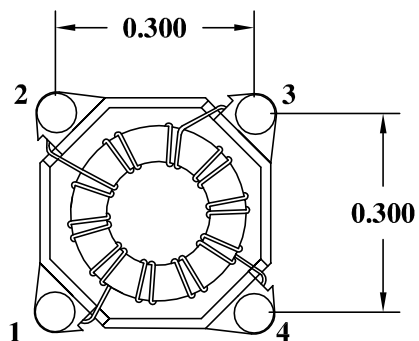
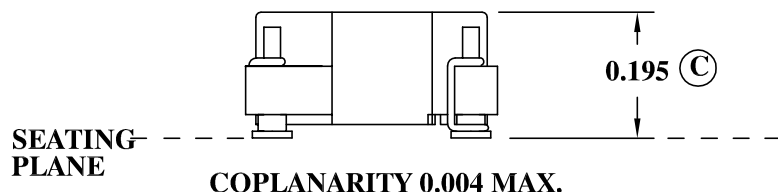
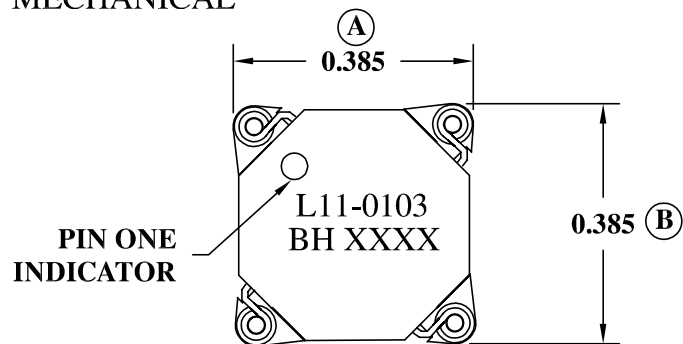
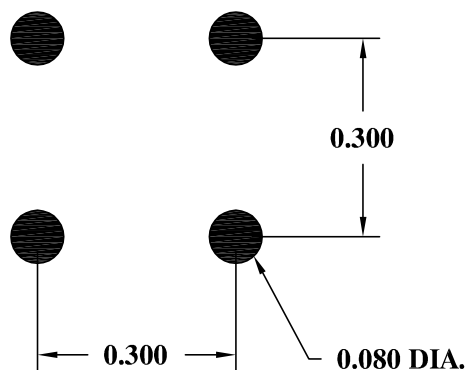


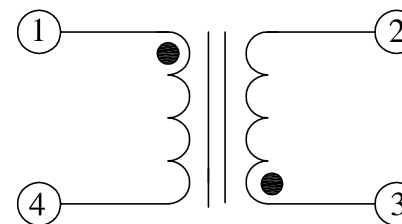
MECHANICAL



RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 150µH NOM. (1-2) (SH 1, 4) (SH 2, 3)
10 KHz 100mV


DC RESISTANCE: 0.010 OHMS NOM. (1-2) (SH 1, 4) (SH 2, 3)

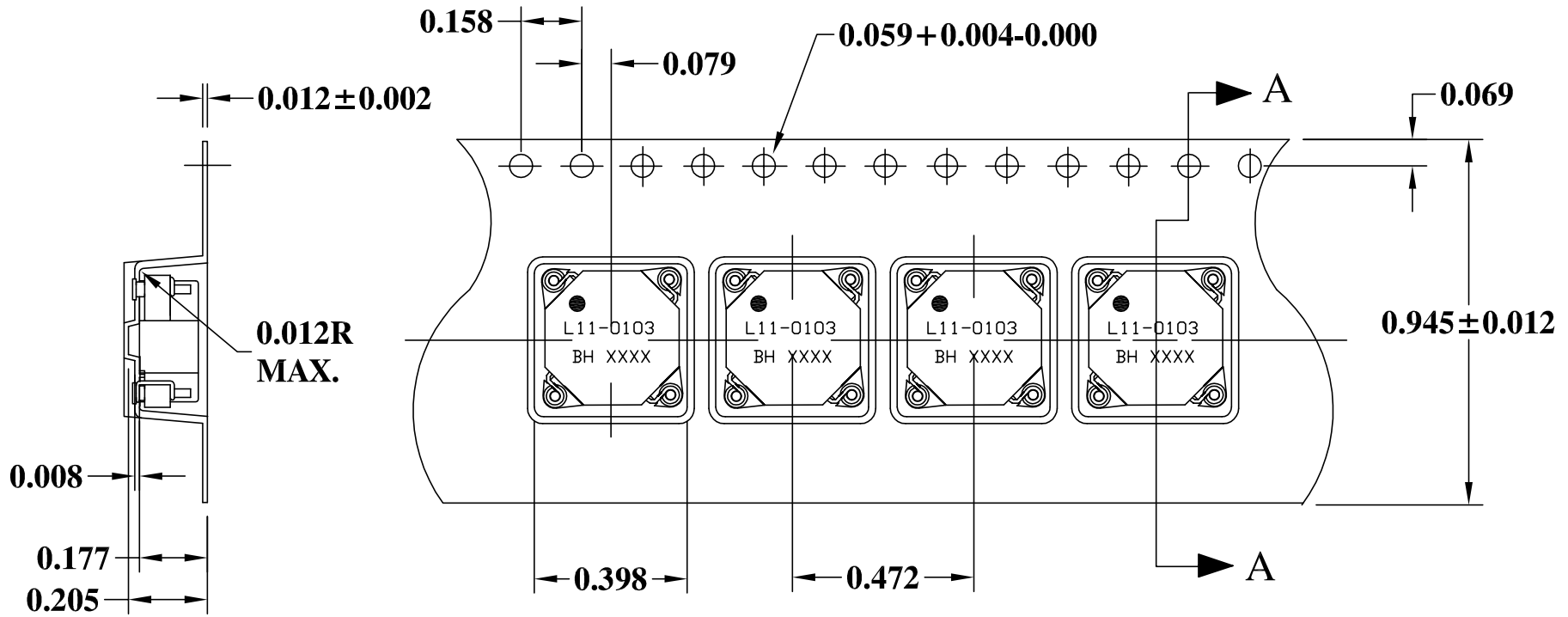
IR: 1 KMEGOHMS MIN. @ 500 VDC (1-3)

IMPEDANCE: 1 MHz - 200 OHMS MIN.
10 MHz - 600 OHMS MIN.
100 MHz - 450 OHMS MIN.


CURRENT RATING: 3.6A

RoHS COMPLIANT

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics		
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY <input type="checkbox"/> X			
				DRAWN:	K. LOFTUS		
				ENGR:	J. DECRAMER		
B	3-7-12	REVISE MECHANICAL	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:	COMMON MODE CHOKE
A	5-13-10	CUSTOMER PRINT	JDC			PART NO.	L11-0103
ISSUE	DATE	REVISION	BY			PAGE 1 OF 2	



811-0187

				DESIGN PER:		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics	
					<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY <input type="checkbox"/> X		
				DRAWN:	K. LOFTUS		
				ENGR:	J. DECRAMER		
B	3-7-12	REVISE MECHANICAL	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:	
A	1-17-08	CUSTOMER PRINT	JDC			COMMON MODE CHOKE	
ISSUE	DATE	REVISION	BY			PART NO. L11-0103	
				PAGE 1 OF 2			