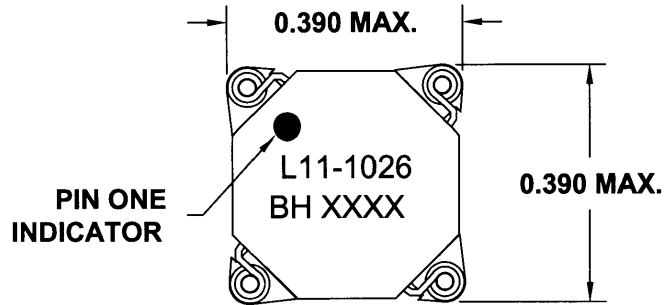
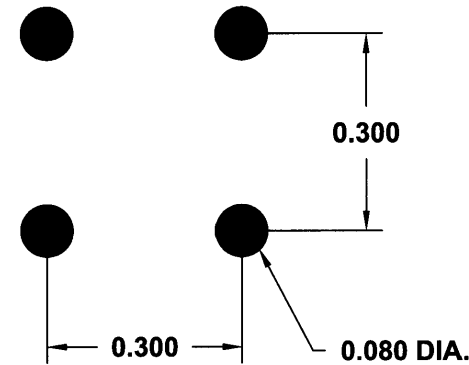


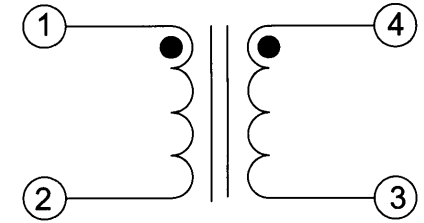
MECHANICAL



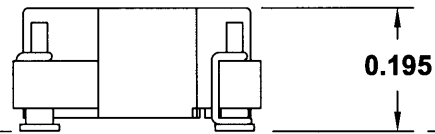
RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



SEATING PLANE



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 89.53 μ H \pm 20% @ 0 ADC (1-2) (SH1, 4) (SH 2, 3)
 71.62 μ H TYP. @ 0.64 ADC (1-2) (SH 1, 4) (SH 2, 3)

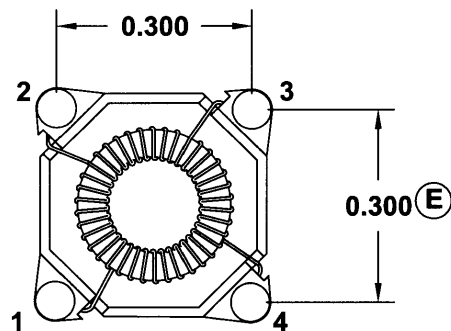
10 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 0.450 μ H MAX. (1-2 SH 4, 3) 120 KHz 50mV

INTERWINDING CAPACITANCE: 65pF NOM. (1-4) 120 KHz 50mV



DC RESISTANCE: 0.6896 OHMS MAX. (1-2) (3-4)

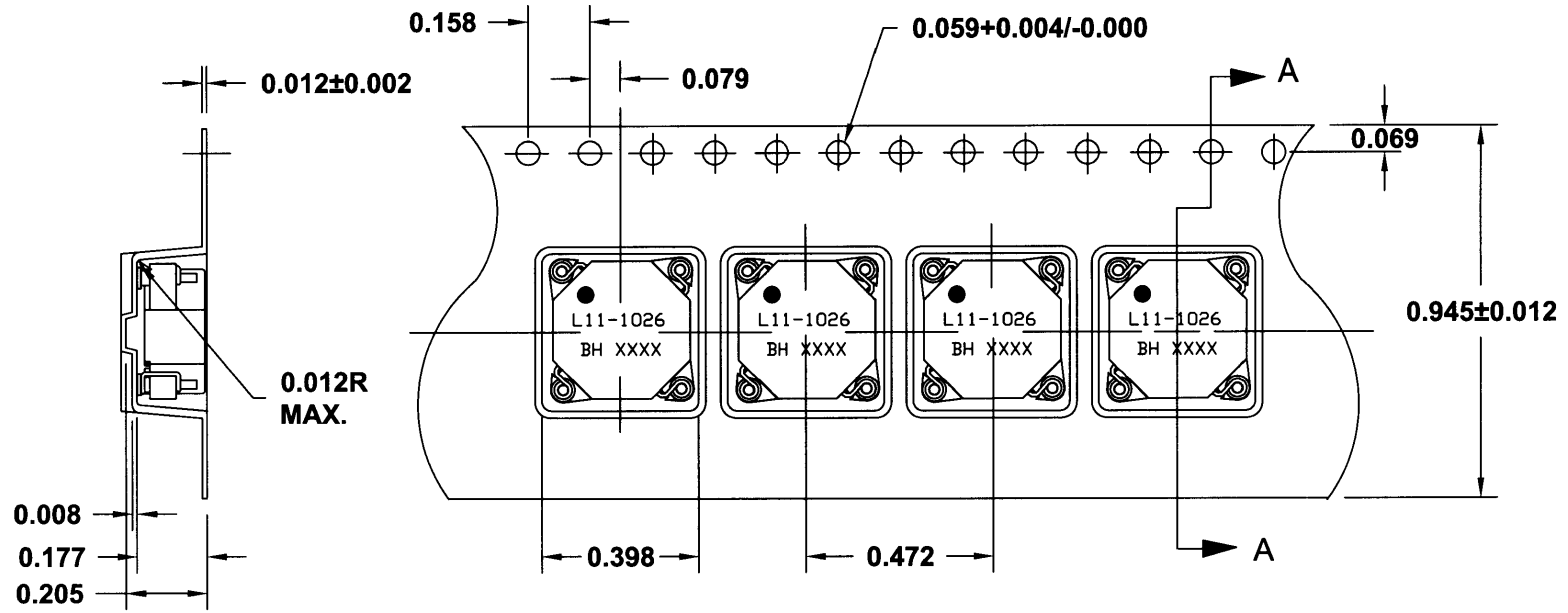
IR: 1K MEGOHMS MIN. @ 500 VDC (1-4)




COPLANARITY 0.004 MAX.

RoHS Compliant

				DESIGN PER:	CATALOG		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM <input type="checkbox"/> NON-PROPRIETARY <input checked="" type="checkbox"/> X			
				DRAWN:	K. LOFTUS		TITLE: INDUCTOR
				ENGR:	J. DECRAMER		
B	6-13-11	REMOVE TARGET POINTS		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005			PART NO. L11-1026
A	4-12-11	CUSTOMER PRINT					
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2			



CARRIER TAPE 811-0187

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				CATALOG		
				<input type="checkbox"/> CUSTOM <input type="checkbox"/> NON-PROPRIETARY <input checked="" type="checkbox"/> X		
				DRAWN: K. LOFTUS		
				ENGR: J. DECRAMER		TITLE: INDUCTOR
B	6-13-11	REMOVE TARGET POINTS	<i>JDC</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. L11-1026
A	4-12-11	CUSTOMER PRINT				
ISSUE	DATE	REVISION	BY			
				PAGE 2 OF 2		