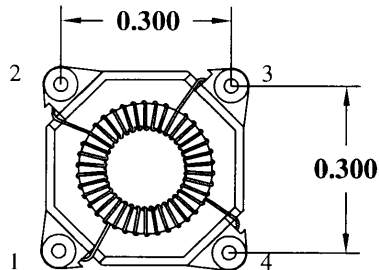
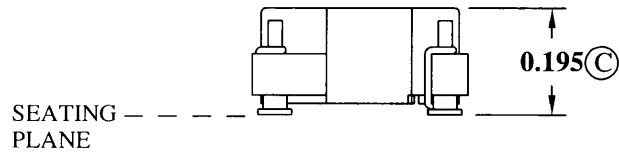
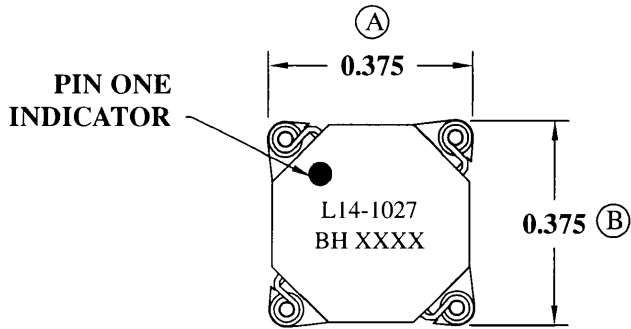
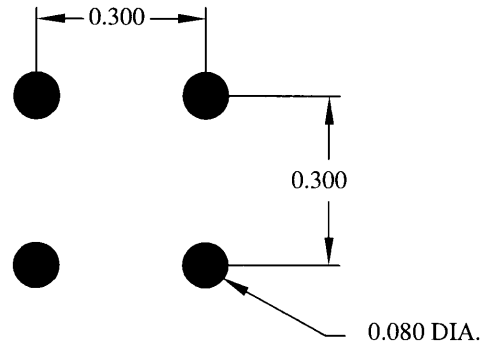


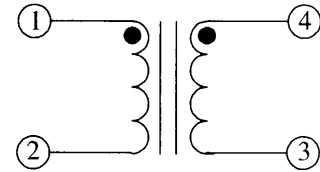
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 11.49 μ H \pm 10% @ 0 ADC (1, 4 - 2, 3)

10 KHz 50mV



DC BIAS: 7.47 μ H TYP. @ 2.93 ADC (1, 4 - 2, 3)

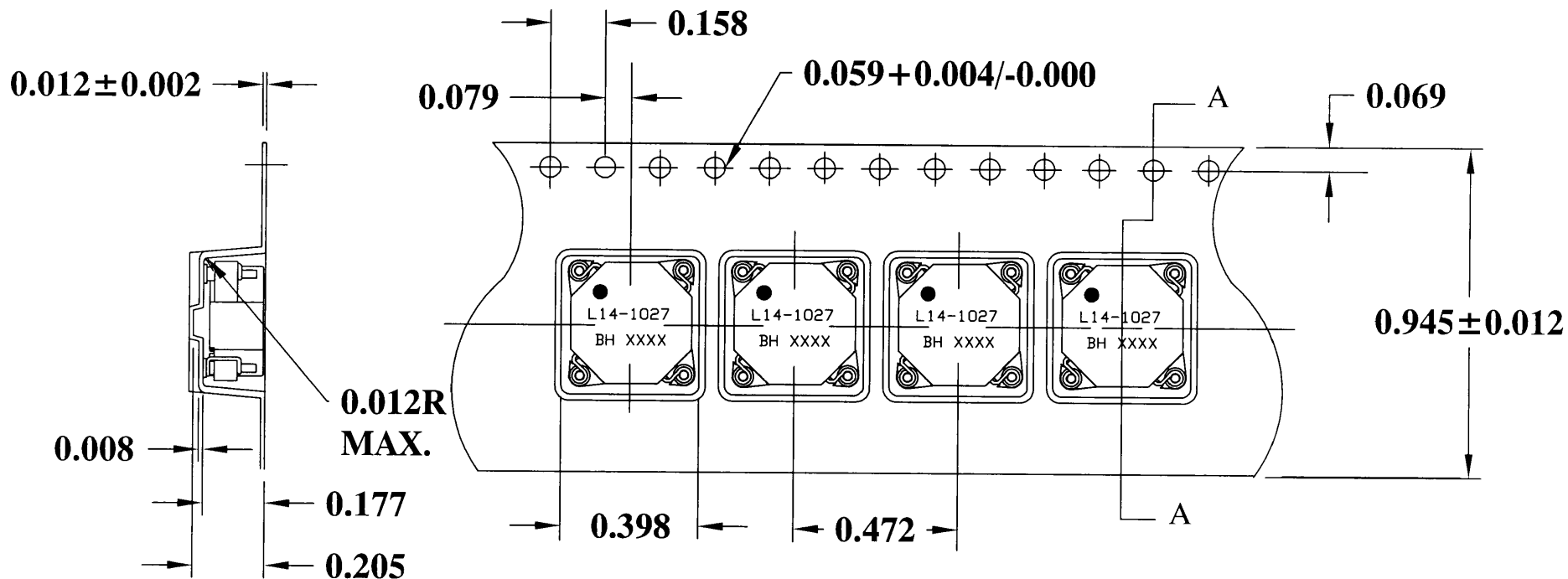
10 KHz 50mV

DC RESISTANCE: 0.082 OHMS MAX. (1, 4 - 2, 3)


IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

				DESIGN PER: BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN: K. LOFTUS	TITLE: INDUCTOR PART NO. L14-1027
				ENGR: J. DECRAMER	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005	
C	10-29-07	REVISE ELECTRICALS			
B	2-9-06	REVISE ELECTRICALS	JDC		
A	2-3-06	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY		



CARRIER TAPE 811-0187

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				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY		
				DRAWN:	K. LOFTUS	TITLE: INDUCTOR
				ENGR:	J. DECRAMER	
C	10-29-07	REVISE ELECTRICALS		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. L14-1027
B	2-9-06	REVISE ELECTRICALS	JDC			
A	2-3-06	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2		