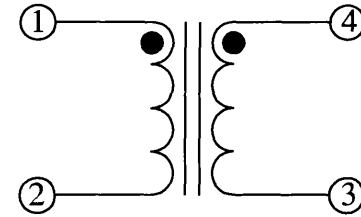
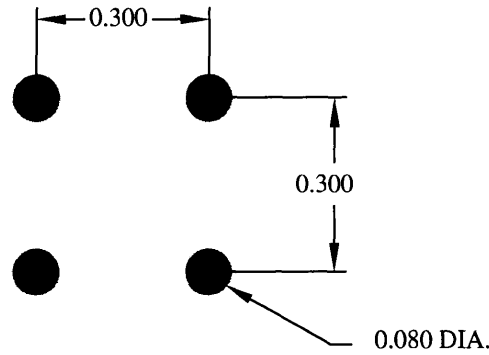
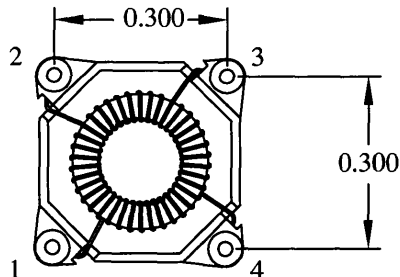
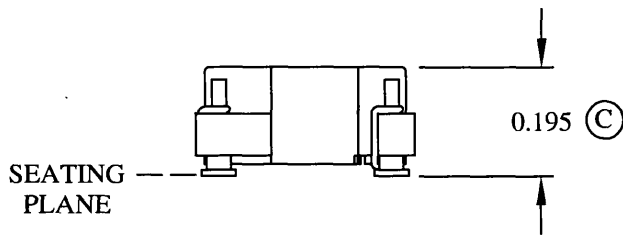
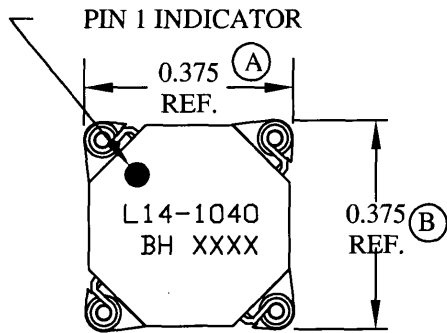


MECHANICAL

SUGGESTED PAD LAYOUT

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1


OPEN CIRCUIT INDUCTANCE: 45.97 μ H \pm 10% @ 0 ADC (1, 4 - 2, 3)
10 KHz 50mV

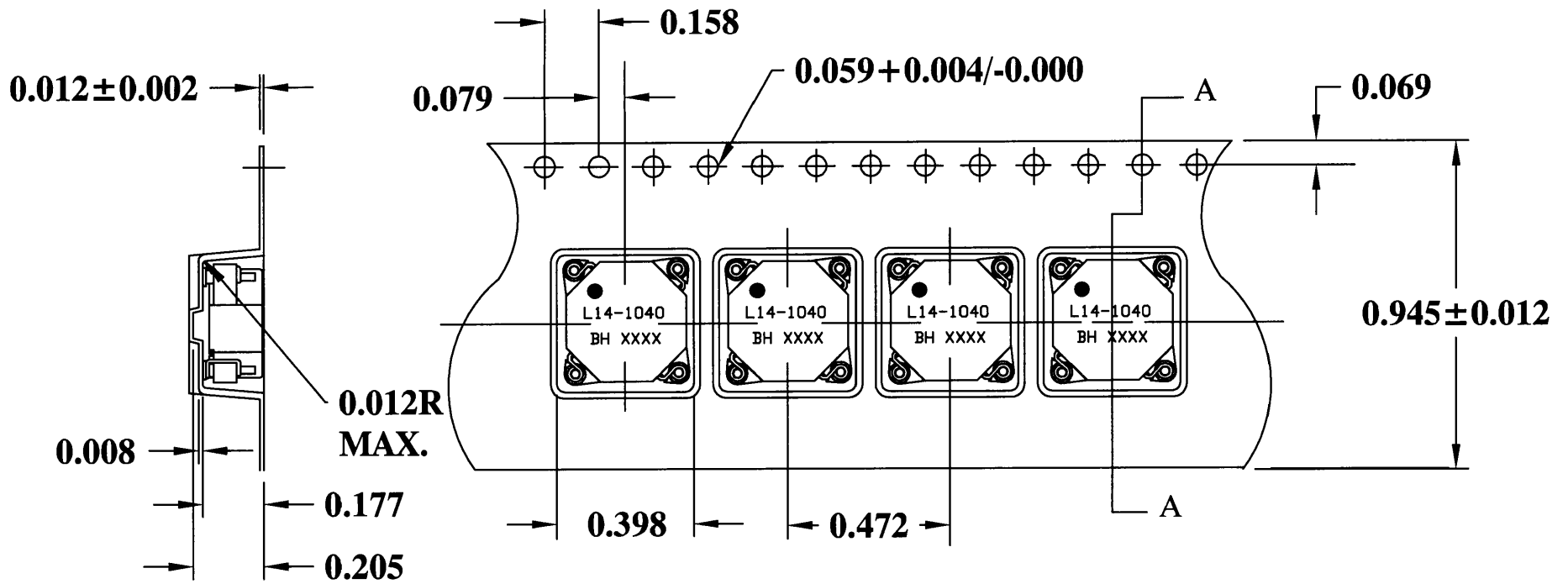
DC BIAS: 29.88 μ H TYP. @ 1.46 ADC (1, 4 - 2, 3)
10 KHz 50mV

DC RESISTANCE: 0.336 OHMS MAX. (1, 4 - 2, 3)


IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	TITLE:
B	8-19-10	DIM. CHANGE	<i>JDM</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005 PAGE 1 OF 2	INDUCTOR
A	10-18-07	CUSTOMER PRINT	JDC		PART NO.
ISSUE	DATE	REVISION	BY		L14-1040



CARRIER TAPE 811-0187

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM	<input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN:	K. LOFTUS	
				ENGR:	J. DECRAMER	
B	8-19-10	DIM. CHANGE		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:
A	10-18-07	CUSTOMER PRINT	JDC			INDUCTOR
ISSUE	DATE	REVISION	BY			PART NO.
						L14-1040