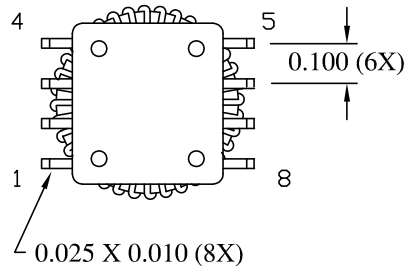
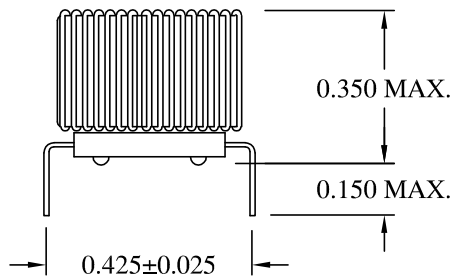
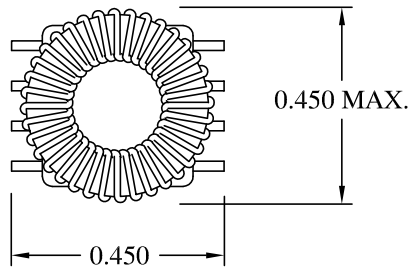
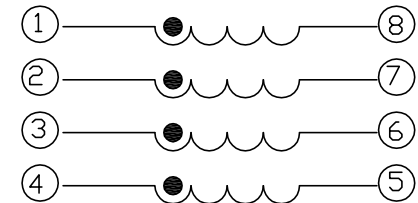


MECHANICAL



SCHEMATIC




ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1:1:1 (TESTED WITH LOC)

OPEN CIRCUIT INDUCTANCE: 18.9 - 35.1μH (1-8) 120 KHz 50mV

INTERWINDING CAPACITANCE: 30pF MAX. (1-5) 120 KHz 50mV

HIGH VOLTAGE TEST: 1000 VDC (WINDING TO WINDING)

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				CATALOG	
			<input type="checkbox"/> CUSTOM <input type="checkbox"/> NON-PROPRIETARY		
			DRAWN: K. LOFTUS		
				ENGR: J. DECRAMER	TITLE: TRANSFORMER
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO. 500-1165
					PAGE 1 OF 1
C	2-8-12	NOW CATALOG	JDC		
B	1-27-97	ECR #3120 ADD 120 KHz 50mV	JDC		
A	7-13-90	CUSTOMER PRINT	TAC		
ISSUE	DATE	REVISION	BY		