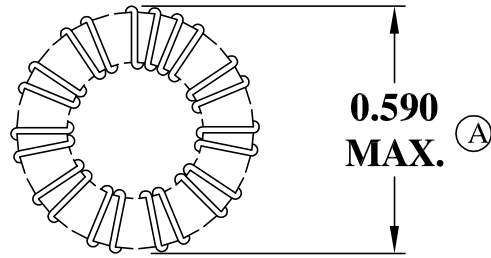
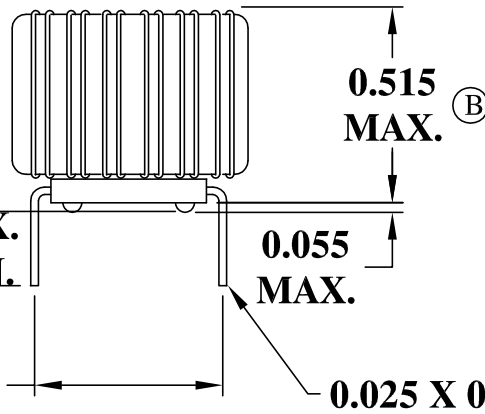


MECHANICAL



0.590
MAX. (A)



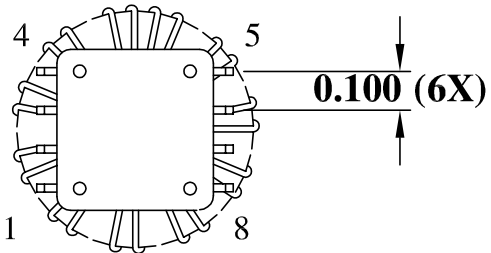
0.150 MAX.
0.125 MIN.

0.515
MAX. (B)

0.055
MAX.

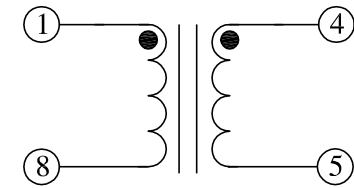
0.025 X 0.010 (8X)

0.425 ± 0.025



0.100 (C)

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 52.3 - 78.4μH (1-8) 100 KHz 50mV


(4-5) READINGS SHOULD BE IDENTICAL

INTERWINDING CAPACITANCE: 30pF MAX.

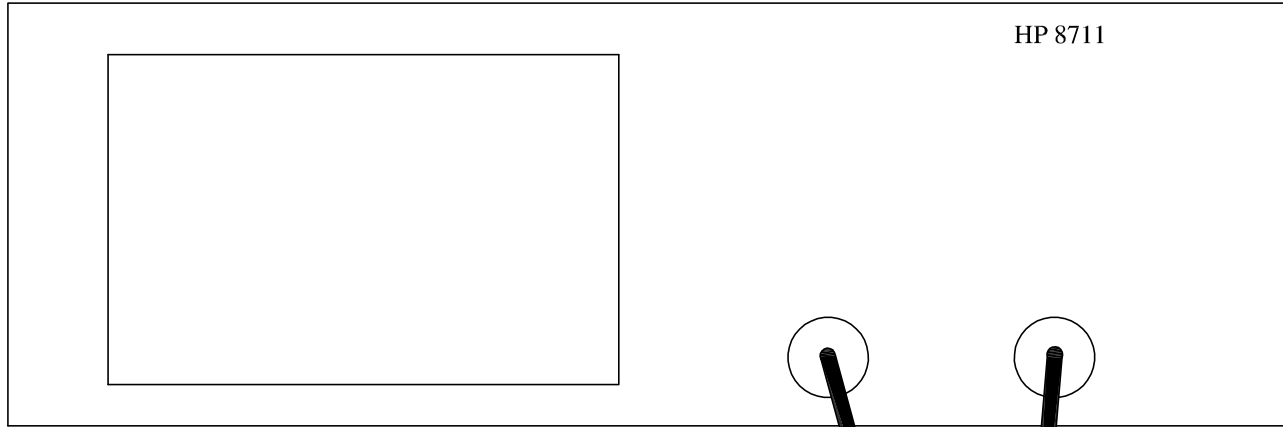
DC RESISTANCE: 0.1 OHMS MAX.

HIGH VOLTAGE TEST: 1000 VDC WINDING TO WINDING

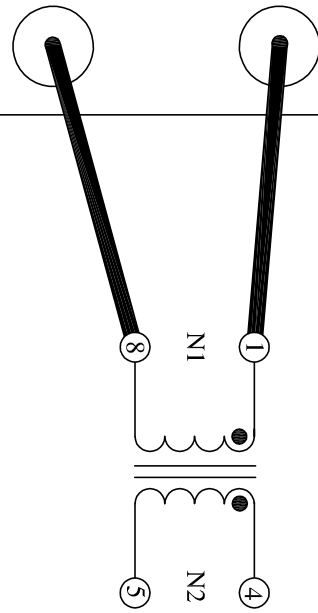
IMPEDANCE: SEE PAGE 2

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
E	2-13-12	NOW BH CATALOG	JDC	<input type="checkbox"/> CUSTOM <input type="checkbox"/> NON-PROPRIETARY		
D	9-9-03	CHG INDUCTANCE AND REMOVE TARGETS	JDC	DRAWN:	K. LOFTUS	TITLE: COMMON MODE CHOKE
C	2-18-00	ADD DIMENSION TARGETS	JDC	ENGR:	J. DECRAMER	
B	1-22-96	ECR #2596 ADD IMPEDANCE TEST	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. 500-1291
A	3-10-92	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2		


IMPEDANCE TEST



FREQUENCY	UPPER LIMIT
2 MHz	-5.0 dB
10 MHz	-15.0 dB
20 MHz	-20.0 dB
40 MHz	-10.0 dB
80 MHz	-5.0 dB
100 MHz	-5.0 dB



500-1291 UNDER TEST

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
E	2-13-12	NOW BH CATALOG	JDC	<input type="checkbox"/> CUSTOM	<input type="checkbox"/> NON-PROPRIETARY	
D	9-9-03	CHG INDUCTANCE AND REMOVE TARGETS	JDC	DRAWN: K. LOFTUS		TITLE: COMMON MODE CHOKE
C	2-18-00	ADD DIMENSION TARGETS	JDC	ENGR: J. DECRAMER		
B	1-22-96	ECR #2596 ADD IMPEDANCE TEST	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. 500-1291
A	3-10-92	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2		