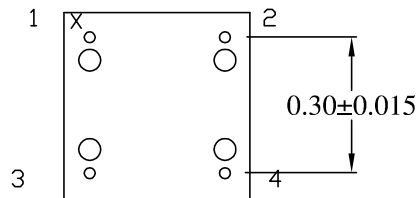
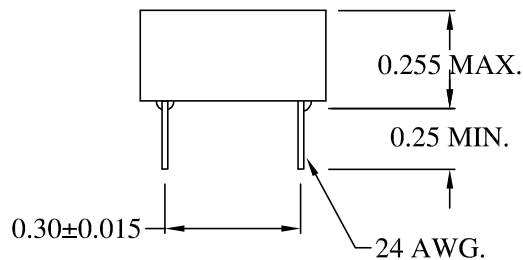
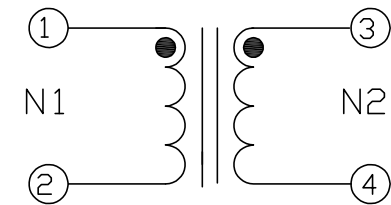
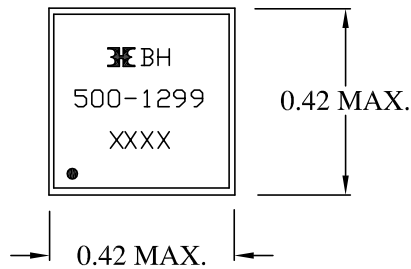


MECHANICAL

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 475 μ H \pm 25% 100 KHz 20mV


PRIMARY LEAKAGE INDUCTANCE: 0.16 μ H MAX.

INTERWINDING CAPACITANCE: 11pF MAX.

DC RESISTANCE @ 25°C: 0.5 OHMS MAX. (1-2) (3-4)

HIGH VOLTAGE TEST: 250 VRMS FOR 1 SEC.

"X" ON BOTTOM OF CASE INDUCATES PIN 1

				DESIGN PER:		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
G	2-13-12	NOW BH CATALOG	JDC		BH CATALOG	
F	11-17-94	ECR #2189 "X" ON PIN 1	RS	<input type="checkbox"/>	CUSTOM	<input type="checkbox"/>
E	9-19-94	ECR #2116 "X" TO CASE	RS		DRAWN: K. LOFTUS	
D	4-12-94	UPDATE TO CADD	RS		ENGR: J. DECRAMER	TITLE:
C	3-24-86	ADD PIN SIZE	JDC		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005	TRANSFORMER
B	2-22-94	SHOW PRODUCTION PART NO	JDC			PART NO.
A	9-24-85	CUSTOMER PRINT	JVM			
ISSUE	DATE	REVISION	BY		PAGE 1 OF 1	