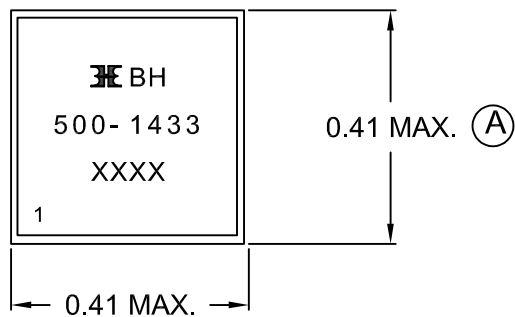
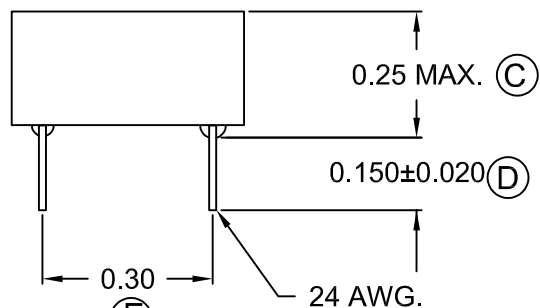


MECHANICAL

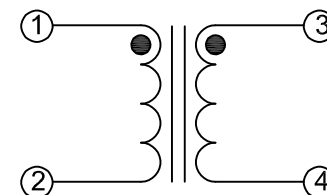
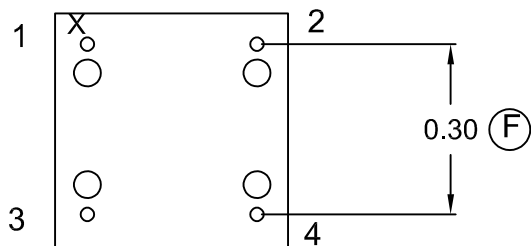
SCHEMATIC



(B)



(E)



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 750uH±25% 100 KHz 20mV


PRIMARY LEAKAGE INDUCTANCE: 0.3uH MAX.

INTERWINDING CAPACITANCE: 15pF MAX.

DC RESISTANCE: 0.30 OHMS MAX. (1-2 AND 3-4)

HIGH VOLTAGE TEST: 250 VRMS FOR 1 SEC.

NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 1.

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
G	3-3-17	ECR #10341 UPDATE LOC	JDC	CATALOG DRAWN: K. LOFTUS ENGR: J. DECRAMER	
F	3-15-01	ECR #4990 UPDATE TO CAD	JDC		
E	11-17-94	"X" INDICATES PIN 1		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 1 OF 1	TITLE:
D	9-19-94	ECR #2116 ADD "X" TO CASE			TITLE:
C	6-1-93	REDO DCR			TITLE:
B	12-18-96	ELECTRICAL CHANGES	JVM		TITLE:
A	1-9-92	CUSTOMER PRINT	JVM		TITLE:
ISSUE	DATE	REVISION	BY		PART NO.
					500-1433