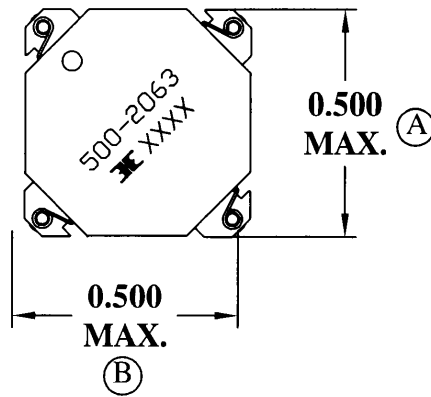
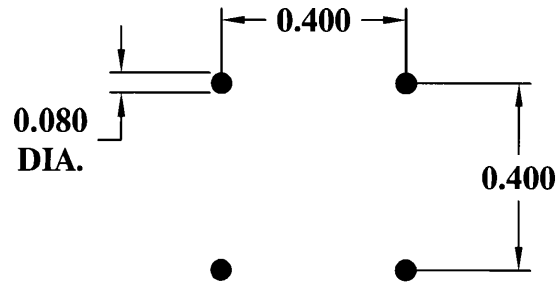


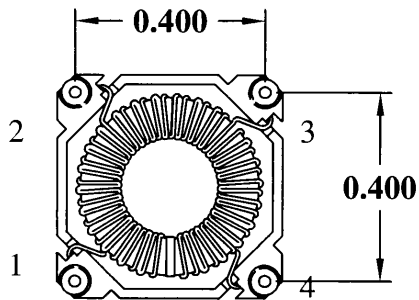
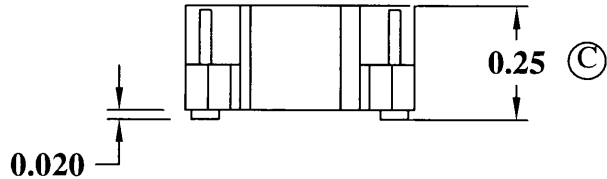
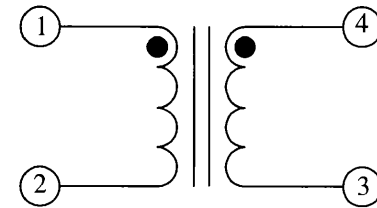
MECHANICAL



RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 64uH NOM. 100 KHz 20mV

DC RESISTANCE: 0.116 OHMS NOM.


HIGH VOLTAGE TEST: 1000 VDC WINDING TO WINDING (1-3)

IMPEDANCE (1-2): 10 MHz 1.5K OHMS MIN.

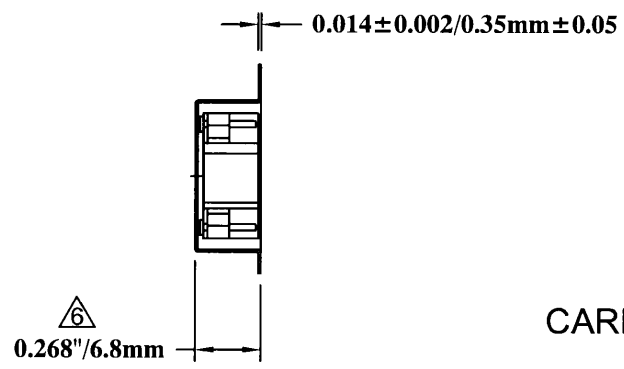
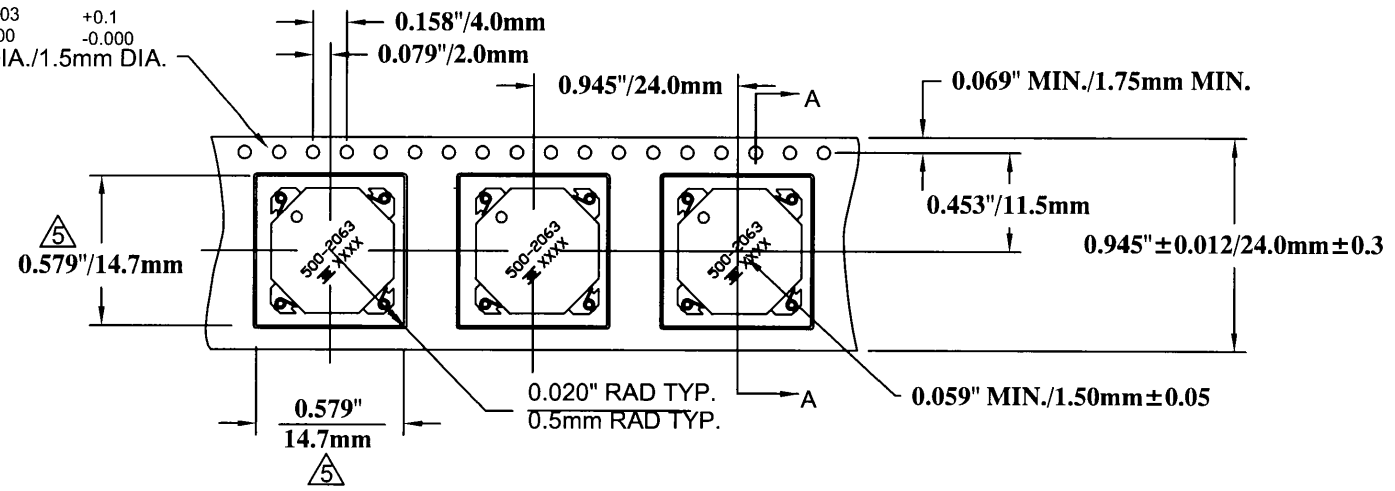
50 MHz 2.1K OHMS MIN.

75 MHz 1.5K OHMS MIN.

100 MHz 1.2K OHMS MIN.


				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				CATALOG	
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN: K. LOFTUS	
K	5-11-06	SHOW NEW CASE		ENGR: J. DECRAMER	TITLE: COMMON MODE DUAL INDUCTOR
J	11-11-03	INSERTION TO IMPEDANCE	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO. 500-2063
H	11-28-00	UPDATE TO CAD	JDC		
ISSUE	DATE	REVISION	BY		

+0.003  
-0.000  
0.059" DIA./1.5mm DIA.



SECTION A-A

CARRIER TAPE 811-0137

				DESIGN PER:	 <b>BH ELECTRONICS, INC.</b> BURNSVILLE & MARSHALL, MN  © 1998 BH Electronics
				<b>CATALOG</b>	
			<input type="checkbox"/> CUSTOM	NON-PROPRIETARY <input checked="" type="checkbox"/>	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	
K	5-11-06	SHOW NEW CASE		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	TITLE: <b>COMMON MODE DUAL INDUCTOR</b>
J	11-11-03	INSERTION TO IMPEDANCE	JDC		PART NO. <b>500-2063</b>
H	11-28-00	UPDATE TO CAD	JDC		
ISSUE	DATE	REVISION	BY		