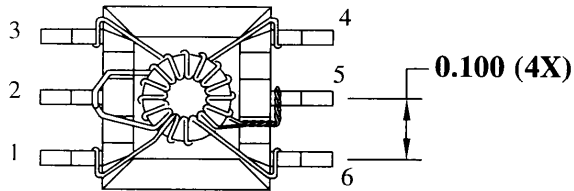
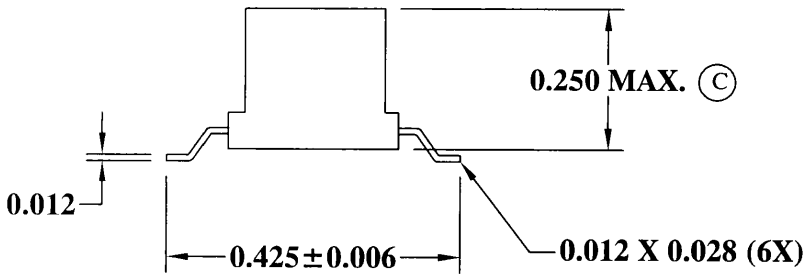
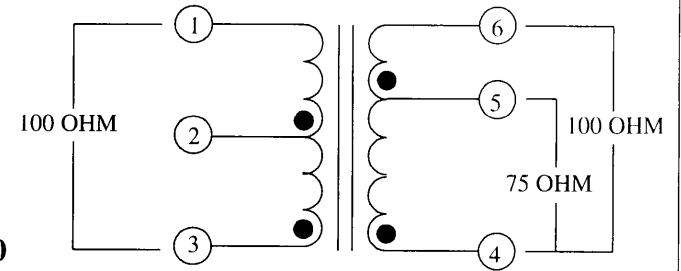
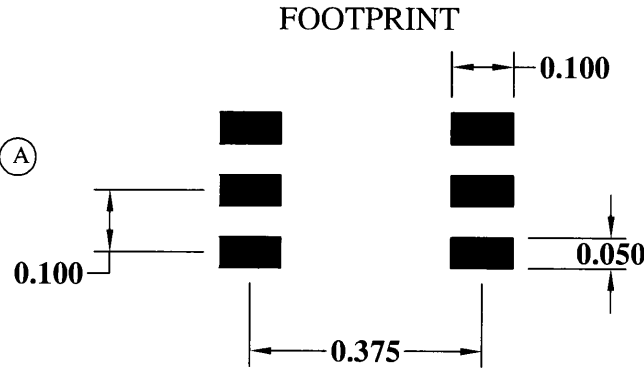
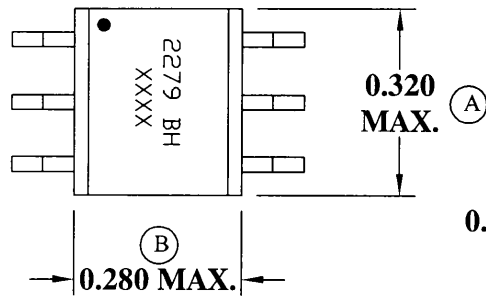


MECHANICAL

SCHEMATIC



ELECTRICAL SPECIFICATIONS

IMPEDANCE RATIO: 75:100 (4-5:1-3) OR 100:100 (4-6:1-3)

HIGH VOLTAGE TEST: 2500 VAC / 1 SEC.

INSERTION LOSS: <1 dB 5 MHz-50 MHz (BOTH IMPEDANCES)

RETURN LOSS: >15dB 5 MHz-30 MHz (BOTH IMPEDANCES)

>12.5dB 30 MHz - 50 MHz (BOTH IMPEDANCES)

DCR: 0.25 OHMS MAX. (1-3) (4-6)


ISSUE	DATE	REVISION	BY
E	3-8-08	REVISE ELECTRICALS	<i>KL</i>
D	12-11-01	ADD IMPEDANCE AND TARGETS	BG
C	7-28-97	NEW CASE	BG
B	2-12-97	Q9677-1 NOW 500-2279	
A	12-4-96	CUSTOMER PRINT	BG

DESIGN PER:
CATALOG

DRAWN: K. LOFTUS
 ENGR: B. GASS

UNLESS OTHERWISE SPECIFIED:
 DIMENSIONS IN INCHES, TOLERANCES
 NON-CUMULATIVE
 .XX±.01 .XXX±.005

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BH ELECTRONICS, INC.
 BURNSVILLE & MARSHALL, MN

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TITLE: **DUAL IMPEDANCE TRANSFORMER**

PART NO. **500-2279**