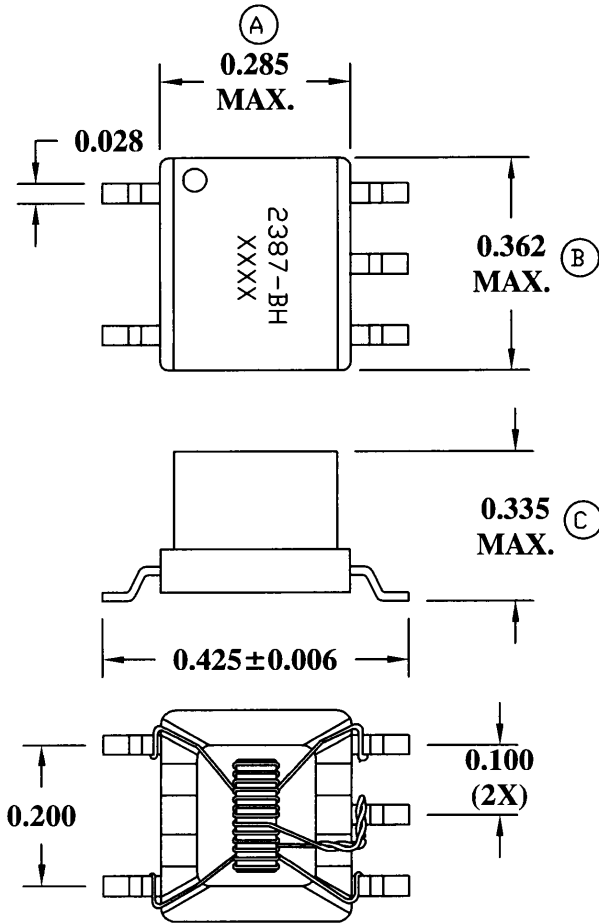
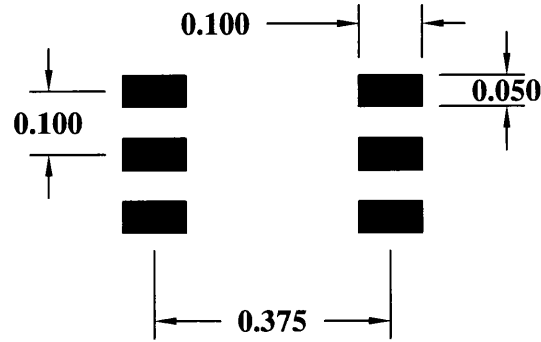


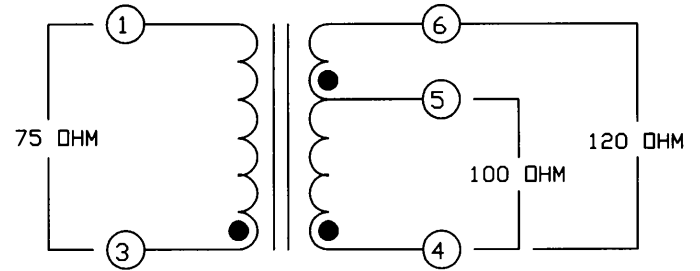
MECHANICAL



FOOTPRINT




SCHEMATIC



ELECTRICAL SPECIFICATIONS

IMPEDANCE RATIO: 75:100 (4-6:3-2) OR 75:120 (4-6:3-1)  
 OPEN CIRCUIT INDUCTANCE: 1mH NOM.  
 INTERWINDING CAPACITANCE: 7pF NOM.  
 DC RESISTANCE: 0.75 OHMS MAX. (4-6) (3-1)  
 HIGH VOLTAGE TEST: 1500 VAC / 1 SEC.  
 INSERTION LOSS: <1dB, 300 KHz TO 10 MHz  
 RETURN LOSS: >15dB, 300 KHz TO 10 MHz

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				CATALOG	
F	8-24-06	DIMENSION CHANGE	B.G.	DRAWN: K. LOFTUS	TITLE: DUAL IMPEDANCE TRANSFORMER
E	1-7-05	REMOVE LABEL	BG	ENGR: B. GASS	
D	12-11-01	ADD TARGETS AND CHG DCR	BG	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO. 500-2387
C	10-15-01	CHANGE TO BH CATALOG	BG		
B	9-29-98	Q10046 NOW 500-2387	BG		
A	5-6-98	CUSTOMER PRINT	BG		
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	