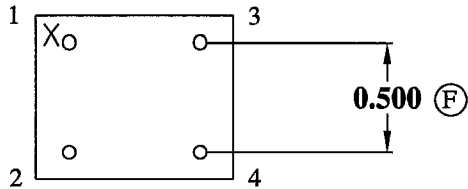
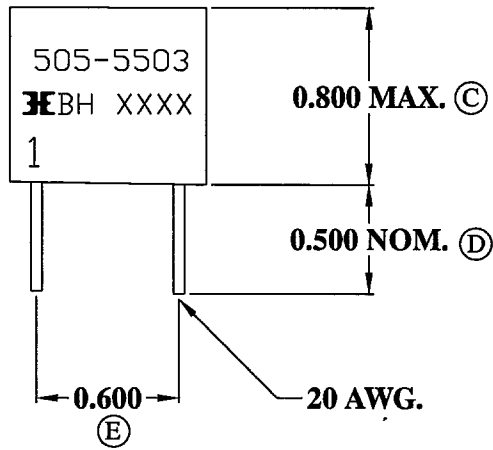
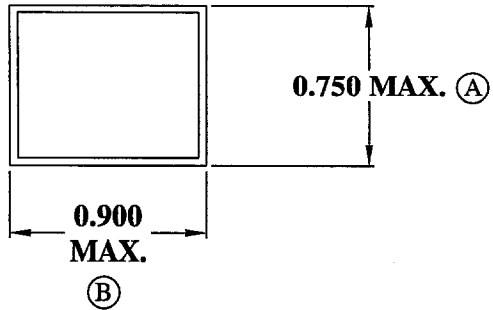
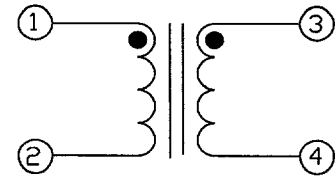


MECHANICAL



NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 1.

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1


OPEN CIRCUIT INDUCTANCE: 20mH MIN. (1-2) 1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 200uH MAX. (1-2 SH 3-4)

INTERWINDING CAPACITANCE: 50pF MAX. (1, 2 - 3, 4)

HIGH VOLTAGE TEST: 2500 VRMS 1 SEC.

ET CONSTANT: 360 Vu SEC. MIN.

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				CATALOG	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 1 OF 1	
B	4-9-01	UPDATE TO CADD	<i>JDC</i>		TITLE: SCR TRIGGER TRANSFORMER
A	8-12-94	CUSTOMER PRINT	JDC		PART NO. 505-5503
ISSUE	DATE	REVISION	BY		