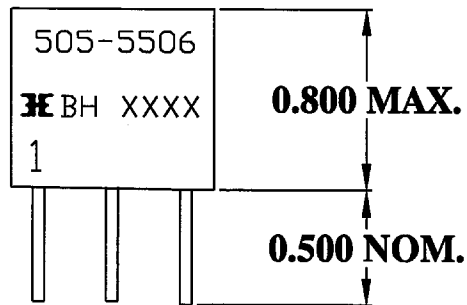
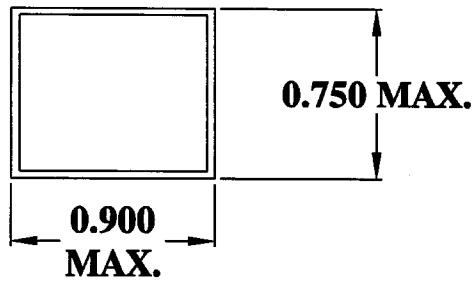
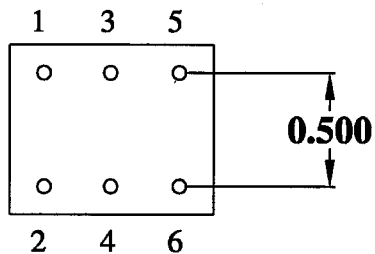


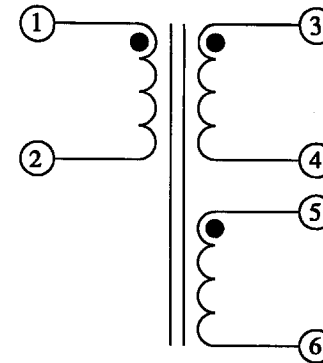
**MECHANICAL**



0.300 (4X) 20 AWG.



**SCHEMATIC**



**ELECTRICAL SPECIFICATIONS**

URNS RATIO: 1:1:1

OPEN CIRCUIT INDUCTANCE: 5.0mH MIN. (1-2) 1 KHz 100mV


PRIMARY LEAKAGE INDUCTANCE: 45µH MAX. (1-2 SHORT 3-4, 5-6)

INTERWINDING CAPACITANCE: 30pF MAX.

(1-3 SH 1-2, 3-4) (1-5 SH 1-2, 5-6)

HIGH VOLTAGE TEST: 2500 VRMS / 1 SEC. (1-3, 5) (3-5)

ET CONSTANT: 180 Vu SEC. MIN.

				DESIGN PER:	 <b>BH ELECTRONICS, INC.</b> BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<b>CATALOG</b>	
				DRAWN: K. LOFTUS	
				ENGR: J. DECRAMER	
B	7-2-01	ECR #5255 UPDATE TO CADD	<i>JDC</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	TITLE:
A	12-22-92	CUSTOMER PRINT	JDC		<b>SCR TRIGGER TRANSFORMER</b>
ISSUE	DATE	REVISION	BY		PART NO.
				PAGE 1 OF 1	<b>505-5506</b>