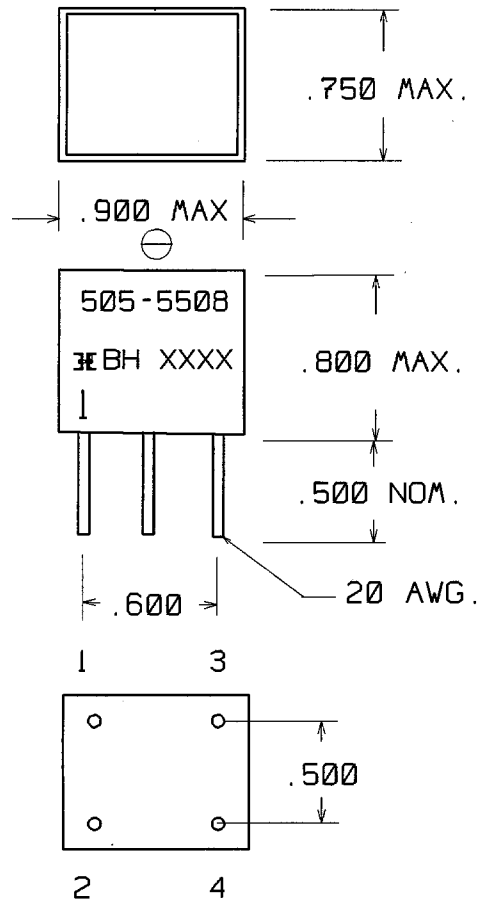
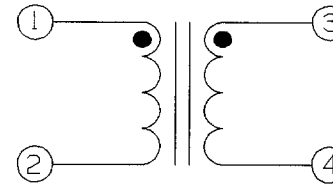


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 2:1


OPEN CIRCUIT INDUCTANCE: 1.0mH MIN. (1-2)
1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 40uH MAX.
(1-2 SHORT 3-4)

INTERWINDING CAPACITANCE: 30pF MAX.
(1-2 TO 3-4)

HIGH VOLTAGE TEST: 2500 VRMS / 1 SEC.

ET CONSTANT: 90V_u SEC. MIN.

			DESIGNED PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN ©1998 BH Electronics
			BH CATALOG	
			DRAWN: K. LOFTUS	
			ENGR.	TITLE
			UNLESS OTHERWISE SPECIFIED; DIMENSIONS IN INCHES. TOLERANCES NON-CUMULATIVE.	SCR TRIGGER TRANSFORMER
A	8-16-99	CUSTOMER PRINT	.XX ± .01 .XXX ± .005	PART NO.
ISSUE	DATE	REVISIONS	SCALE 1:1.1	SHEET 1 / 1
				505-5508