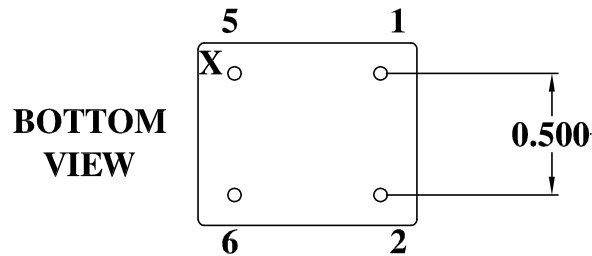
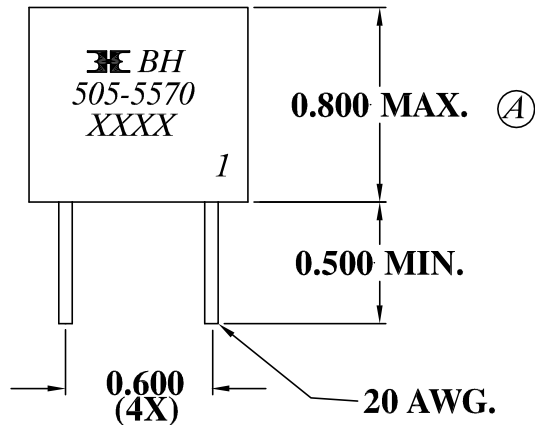
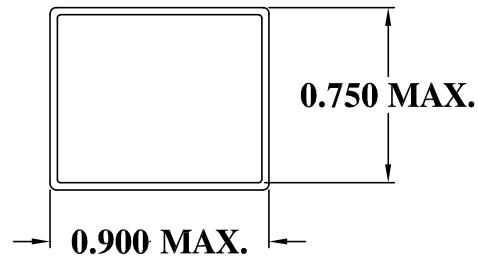
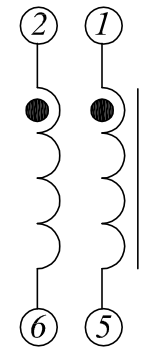


MECHANICAL



BOTTOM VIEW


SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1
OPEN CIRCUIT INDUCTANCE: 0.25mH MIN. (1-5) 50 KHz 100mV
PRIMARY LEAKAGE INDUCTANCE: 5uH MAX.
 (1-5 SHORT 2-6) 120 KHz 50mV
INTERWINDING CAPACITANCE: 20pF MAX. (1-2) 120 KHz 50mV
DC RESISTANCE: 1.0 OHMS MAX. (1-5) (2-6)
HIGH VOLTAGE TEST: 2500 VRMS @ 60 Hz
ET CONSTANT: 45 Vu SEC. MIN.

NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 5.

| | | | | | | |
|-------|---------|-----------------------|-----|---|---|--------------------|
| | | | | DESIGNED PER: |  ® BH Electronics. Inc. Burnsville & Marshall, MN © 1998 BH Electronics | |
| | | | | BH CATALOG | | |
| D | 2-13-12 | NOW BH CATALOG | JDC | DRAWN Kathy Loftus | | |
| C | 11-6-00 | UPDATE TO CAD | JDC | ENGR. J. DECRAMER | | |
| B | 4-29-93 | ECR #1662 PIN ONE "X" | JDC | UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005 | TITLE: | TRANSFORMER |
| A | 1-20-92 | CUSTOMER PRINT | JDC | | PART NO. | 505-5570 |
| ISSUE | DATE | REVISION | BY | | | |