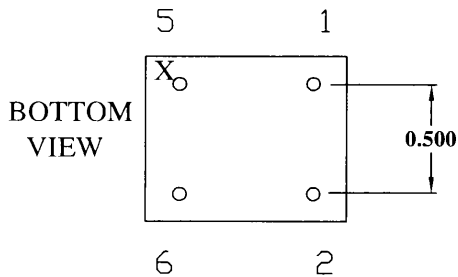
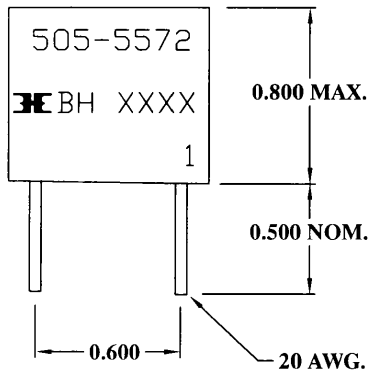
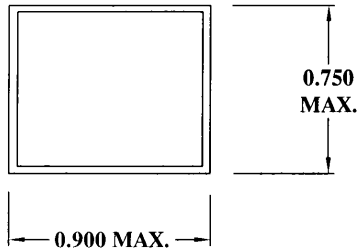
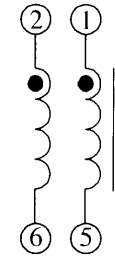


MECHANICAL



NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 5.

SCHEMATIC



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 1.0mH MIN. (1-5)


PRIMARY LEAKAGE INDUCTANCE: 20μH MAX.

INTERWINDING CAPACITANCE: 30pF MAX.

DC RESISTANCE: 2.0 OHMS MAX.

HIGH VOLTAGE TEST: 2500 VRMS

ET CONSTANT: 90 Vμ SEC. MIN.

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM <input type="checkbox"/> NON-PROPRIETARY <input checked="" type="checkbox"/> X		
				DRAWN:	K. LOFTUS	TITLE: TRANSFORMER, SCR
				ENGR:	J. DECRAMER	
B	12-20-06	ECR #7202 UPDATE TO CADD	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. 505-5572
A	9-18-97	CUSTOMER PRINT	JDC			
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		