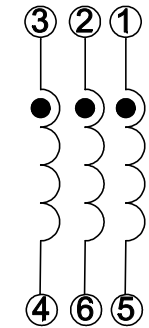
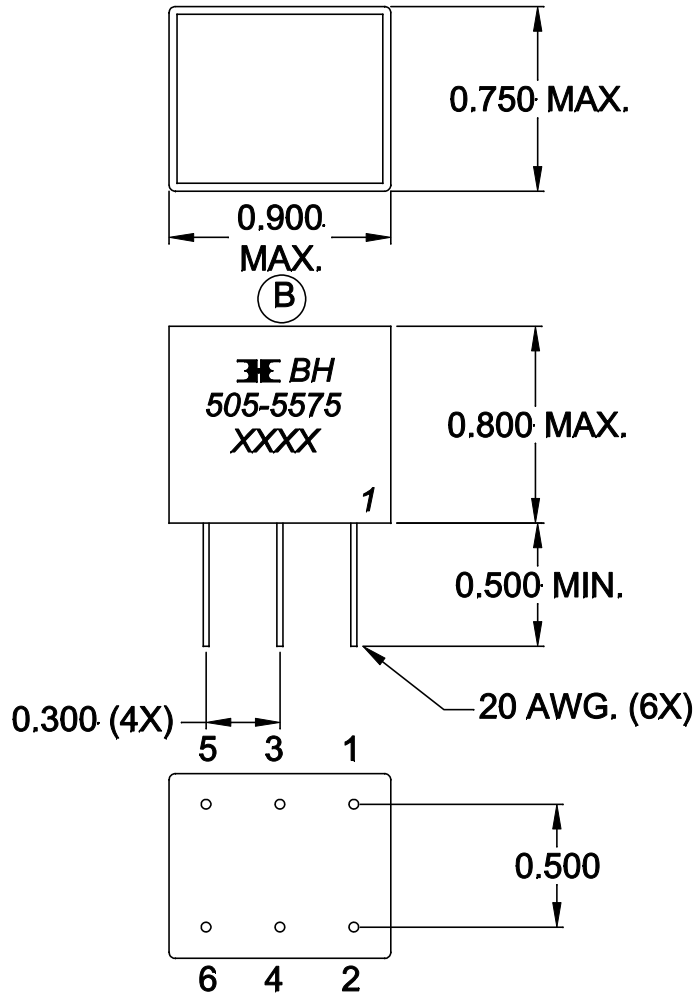


MECHANICAL

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1:1±1.5%

OPEN CIRCUIT INDUCTANCE: 5mH MIN. (1-5) 1KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 45uH MAX. 1 KHz 100mV
(1-5 SH 2-6, 3, 4)

INTERWINDING CAPACITANCE: 30pF MAX. 1KHz 100mV
1-5 (2-6) 1-5 (3-4)

DC RESISTANCE: 3 OHMS MAX. (3-4) (2-6) 1-5)

HIGH VOLTAGE TEST: 2500 VRMS FOR 1 SEC.

DESIGN PER:

BH CATALOG



BHELECTRONICS, INC.
BURNSVILLE & MARSHALL, MN

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DRAWN: K. LOFTUS

ENGR: J. DECRAMER

TITLE:

TRANSFORMER

UNLESS OTHERWISE SPEIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE
.XX±.01 .XXX±.005

PART NO.

505-5575

ISSUE	DATE	REVISION	BY
D	2-13-12	NOW BH CATALOG	JDC
C	10-10-03	ADD TO TEST SPECS.	JDC
B	6-1-00	ADD DIMENSION TARGETS	JDC
A	7-14-90	CUSTOMER PRINT	JDC