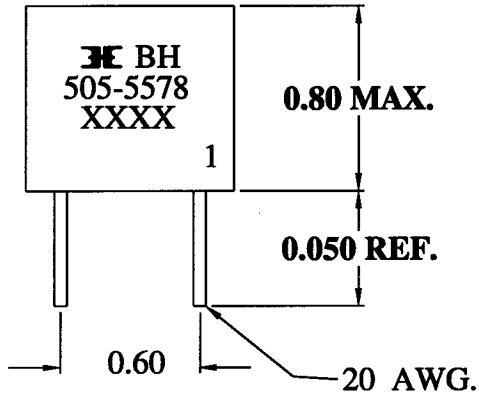
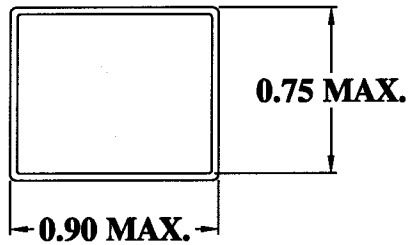
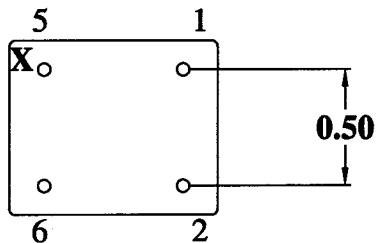


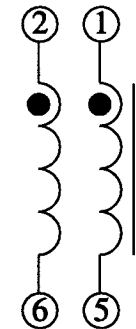
MECHANICAL



BOTTOM VIEW



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 2:1

OPEN CIRCUIT INDUCTANCE: 1.0mH MIN.

PRIMARY LEAKAGE INDUCTANCE: 40μH MAX.

INTERWINDING CAPACITANCE: 30pF MAX.


DC RESISTANCE: 2.0 OHMS MAX.

1.0 OHMS MAX.

HIGH VOLTAGE TEST: 2500 VRMS

ET CONSTANT: 90 Vμ SEC. MIN.

NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 5.

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				DRAWN: K. LOFTUS	TITLE: SCR TRANSFORMER
				ENGR: J. DECRAMER	
B	4-10-02	REDO ET SPEC.	<i>JDC</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO. 505-5578
A	1-27-97	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	