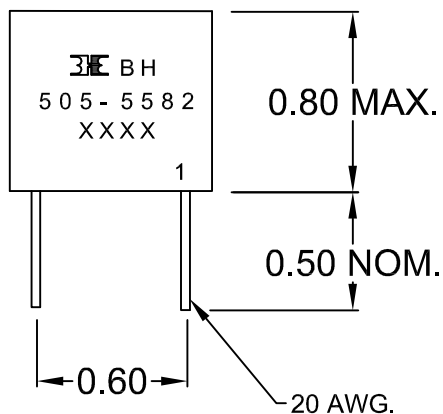
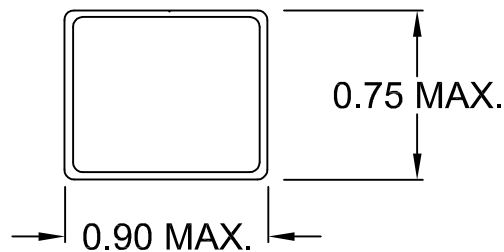
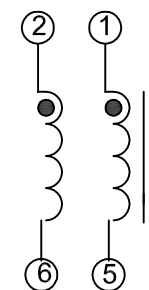
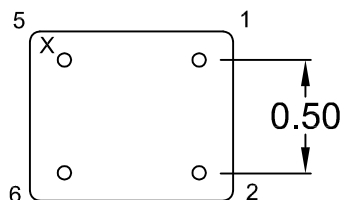


SCHEMATIC
FIG. 1



BOTTOM
VIEW



ELECTRICAL REQUIREMENTS

URNS RATIO: 2:1

OPEN CIRCUIT INDUCTANCE: 20mH MIN. (1-5) 1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 200μH MAX. 120 KHz 50mV

INTERTWINING CAPACITANCE: 75pF MAX. 120 KHz 50mV

DC RESISTANCE: 5.0 OHMS MAX.

3.0 OHMS MAX.

HIGH VOLTAGE TEST: 2500 VRMS

ET CONSTANT: 360 Vμ SEC. MIN.

NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 5

DESIGN PER:

BH CATALOG



BH ELECTRONICS, INC.
BURNSVILLE & MARSHALL, MN

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DRAWN: KALANI SA

ENGR: J. DECRAMER

TITLE:

TRANSFORMER, SCR

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE
.XX±.01 .XXX±.005

PART NO.

505-5582

PAGE 1 OF 1

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