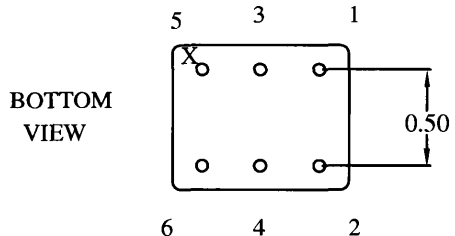
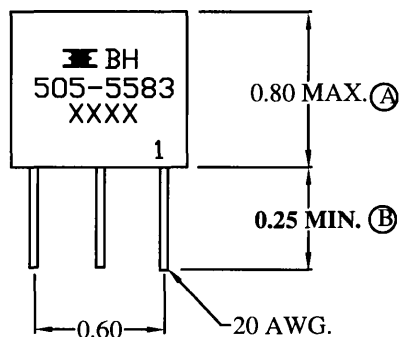
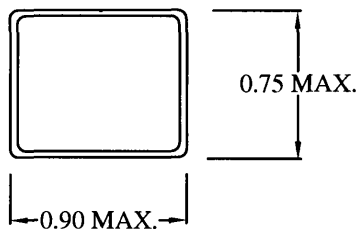
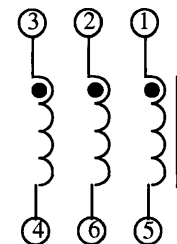


**MECHANICAL**



NOTE: "X" ON BOTTOM OF CASE INDUCATES PIN 5.

**SCHEMATIC**



**ELECTRICAL SPECIFICATIONS**

URNS RATIO: 2:1:1

OPEN CIRCUIT INDUCTANCE: 20.0mH MIN. (1-5) 1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 200μH MAX. (1-5 SH 2, 3, 4, 6)  
120 KHz 50mV



INTERWINDING CAPACITANCE: 75pF MAX. (5-6) 120 KHz 50mV

DC RESISTANCE: 5.0 OHMS MAX. (1-5)

2.7 OHMS MAX. (2-6) (3-4)

HIGH VOLTAGE TEST: 2500 VRMS @ 60Hz (1-2, 3) (2-3)

ET CONSTANT: 360 Vμ SEC. MIN.

				DESIGN PER:	BH CATALOG	 <b>BH ELECTRONICS, INC.</b> BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM	<input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN:	K. LOFTUS	TITLE: <b>SCR TRIGGER TRANSFORMER</b>
				ENGR:	J. DECRAMER	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO. <b>505-5583</b>
A	4-24-12	CUSTOMER PRINT				
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		