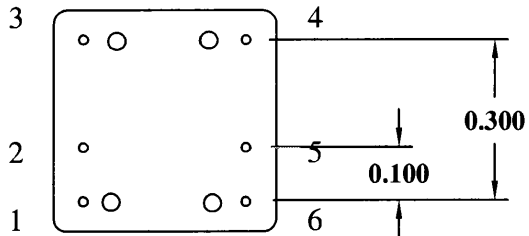
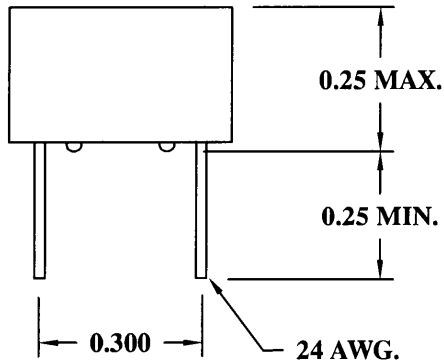
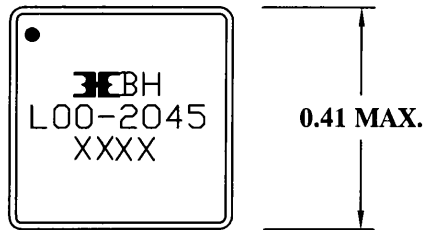
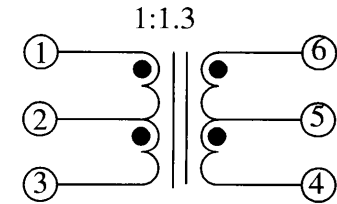


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1CT:1.3CT

OPEN CIRCUIT INDUCTANCE: 400μH MIN. (1-3)

PRIMARY LEAKAGE INDUCTANCE: 3μH MAX.


INTERWINDING CAPACITANCE: 25pF NOM.

DC RESISTANCE: 1 OHM MAX. (1-3)

1.3 OHMS MAX. (4-6)

HIGH VOLTAGE TEST: 500 VRMS

ET CONSTANT: 18.3 Vμ SEC. MIN.

				DESIGN PER:	CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM	NON-PROPRIETARY <input checked="" type="checkbox"/>	
				DRAWN:	K. LOFTUS	TITLE: ISOLATION TRANSFORMER
				ENGR:	B. MARKS	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		PART NO.
A	12-14-06	CUSTOMER PRINT	<i>BOM</i>			L00-2045
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		