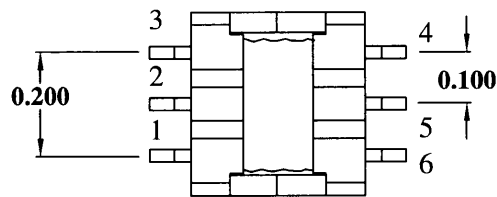
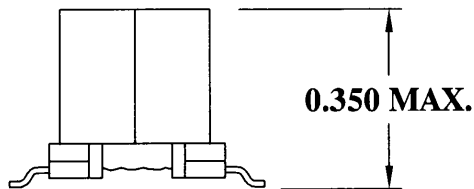
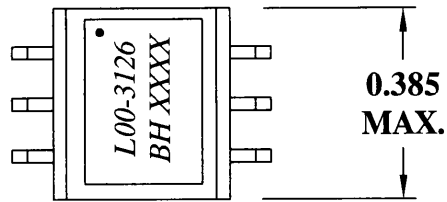
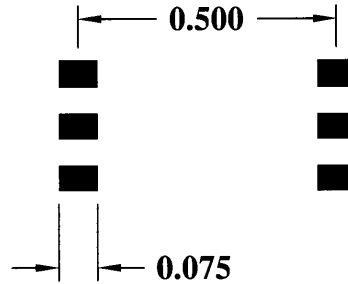


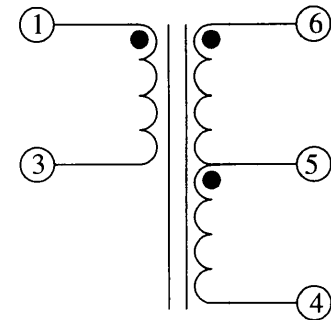
MECHANICAL



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS: 1:6

OPEN CIRCUIT INDUCTANCE: 72 - 88 μ H (1-3) 100 KHz 100mV


PRIMARY LEAKAGE INDUCTANCE: 0.38 μ H NOM. (1-3 SH 4, 5, 6) 120 KHz 50mV

INTERWINDING CAPACITANCE: 40pF NOM. (1-6) 120 KHz 50mV

DC RESISTANCE: 0.155 OHMS NOM. (1-3) 9.43 OHMS NOM. (6-4)

SRF: 681 KHz NOM. (4-6)

HIGH VOLTAGE TEST: 1600 VAC (1-6)

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM	<input checked="" type="checkbox"/> NON-PROPRIETARY	
D	8-3-10	REVISE MECHANICALS	JDC	DRAWN:	K. LOFTUS	
C	5-11-10	TURNS CHANGE	JDC	ENGR:	J. DECRAMER	
B	4-20-10	Q12713 NOW L00-3126	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005		TITLE:
A	4-14-10	CUSTOMER PRINT	JDC			PART NO.
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1		