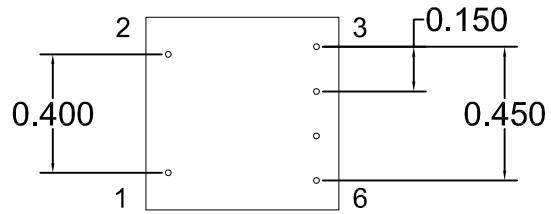
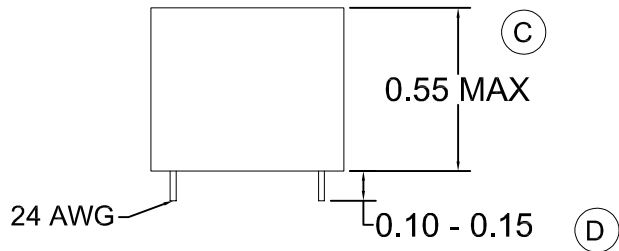
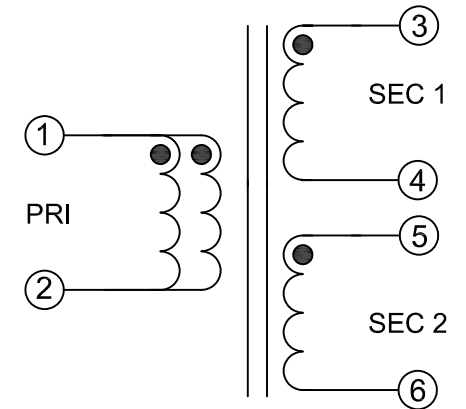
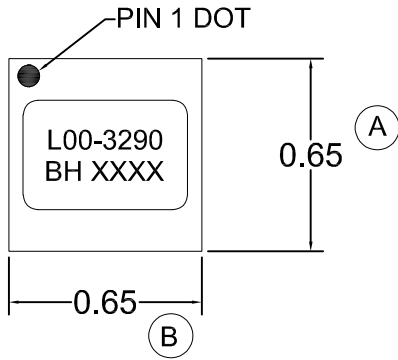


MECHANICAL

SCHEMATIC



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 20:78:78


OPEN CIRCUIT INDUCTANCE: 60 μ H \pm 15% 1 KHz 100mV (1-2)

PRIMARY LEAKAGE INDUCTANCE: 1.0 μ H MAX. 40 KHz 100 mV (1-2 SH 3,4,5,6)

INTERWINDING CAPACITANCE: 75 pF NOM. 120 KHz 50 mV (1-3 SH 1,5 SH 3-4)

DC RESISTANCE: 0.100 OHMS NOM. (1-2)
3.100 OHMS NOM. (3-4)(5-6)

HIGH VOLTAGE TEST: 600 VRMS (1-3,5)

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN: KALANI SA ENGR: J. DECRAMER	
B	5-21-18	CORRECT CI / LABEL	<i>nys</i>	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005 PAGE 1 OF 1	TITLE:
A	8/13/2014	CUSTOMER PRINT	JDC		PART NO.
ISSUE	DATE	REVISION	BY		L00-3290