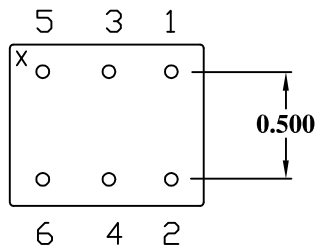
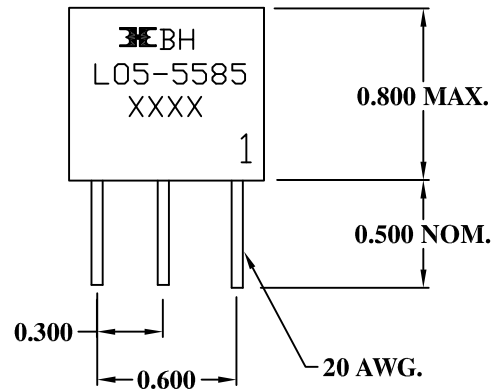
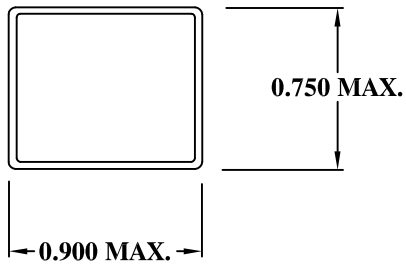


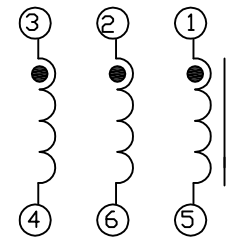
**MECHANICAL**



**BOTTOM VIEW**

NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 5

**SCHEMATIC**



**ELECTRICAL SPECIFICATIONS**

URNS RATIO: 5:1:1

OPEN CIRCUIT INDUCTANCE: 20mH MIN. 1 KHz 100mV

PRIMARY LEAKAGE INDUCTANCE: 200μH MAX.

(1, 5) (SH 2, 3, 4, 6) 120 KHz 50mV

INTERWINDING CAPACITANCE: 50pF MAX. (1-6) 120 KHz 50mV

DC RESISTANCE: 5 OHMS MAX. (1-5)

2 OHMS MAX. (2-6)

2 OHMS MAX. (3-4)

HIGH VOLTAGE TEST: 2500 VRMS @ 60 Hz (1-2, 3) (2-3)

ET CONSTANT: 360 Vμ SEC. MIN.

**(RoHS COMPLIANT VERSION OF 505-5585)**

DESIGN PER:

**BH CATALOG**

CUSTOM  NON-PROPRIETARY

DRAWN: K. LOFTUS

ENGR: J. DECRAMER

UNLESS OTHERWISE SPECIFIED:  
DIMENSIONS IN INCHES, TOLERANCES  
NON-CUMULATIVE  
.XX±.02 .XXX±.005

PAGE 1 OF 1



**BH ELECTRONICS, INC.**  
BURNSVILLE & MARSHALL, MN

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TITLE:  
**SCR TRIGGER TRANSFORMER**

PART NO.  
**L05-5585**

A	7-23-12	CUSTOMER PRINT	JDC
ISSUE	DATE	REVISION	BY