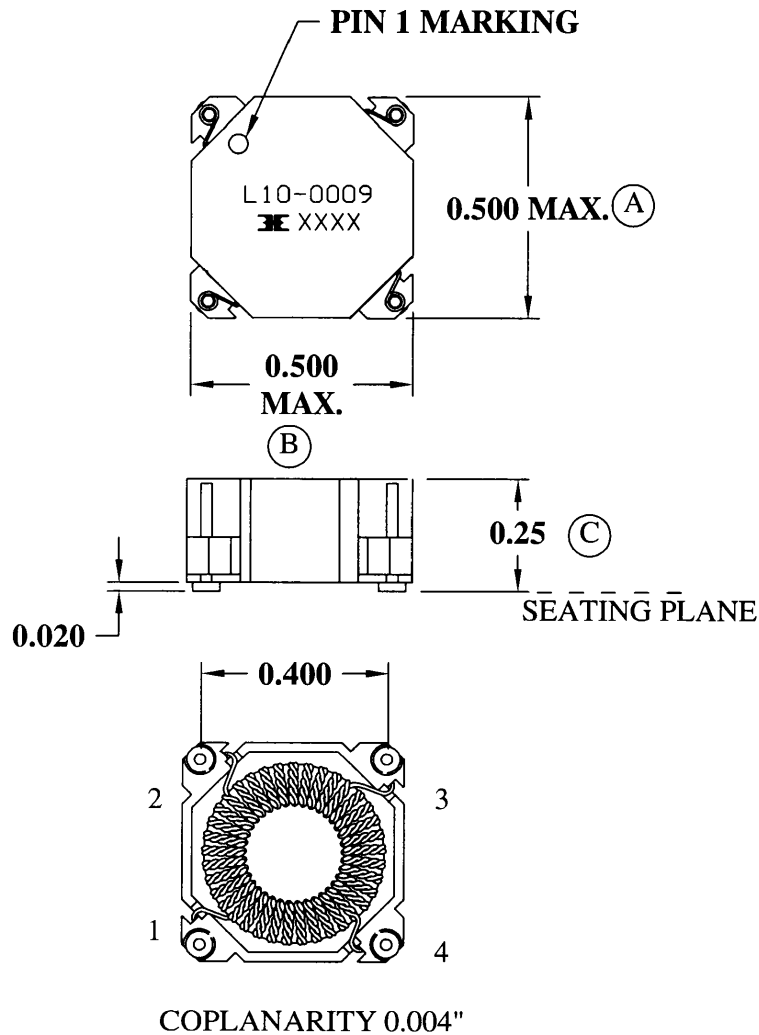
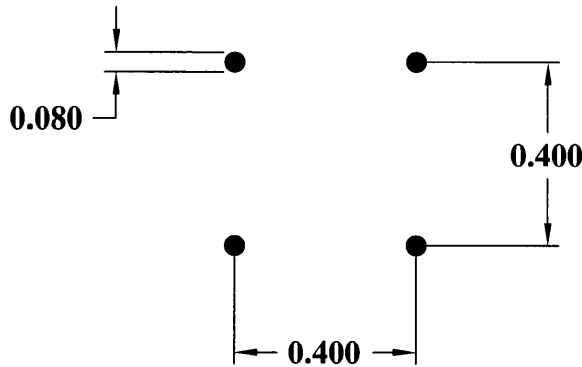


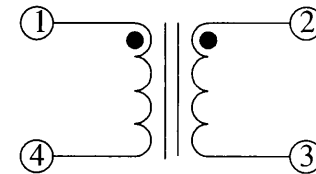
MECHANICAL



RECOMMENDED PCB LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 9mH MIN. @ 0 ADC (1-4) 10 KHz 100mV


PRIMARY LEAKAGE INDUCTANCE: 0.5µH NOM. (1-4 SH 2, 3)
120 KHz 50mV

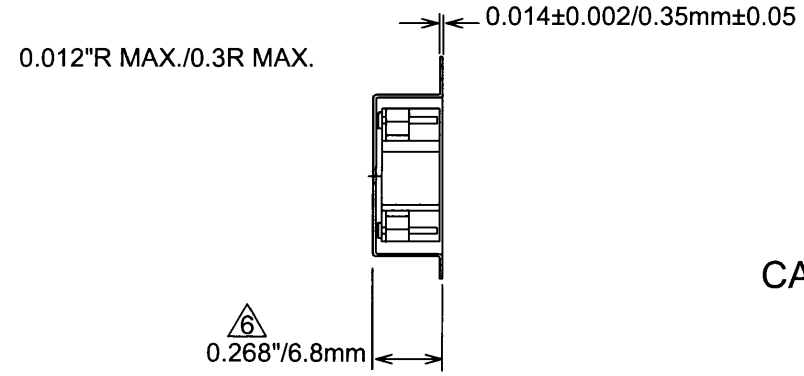
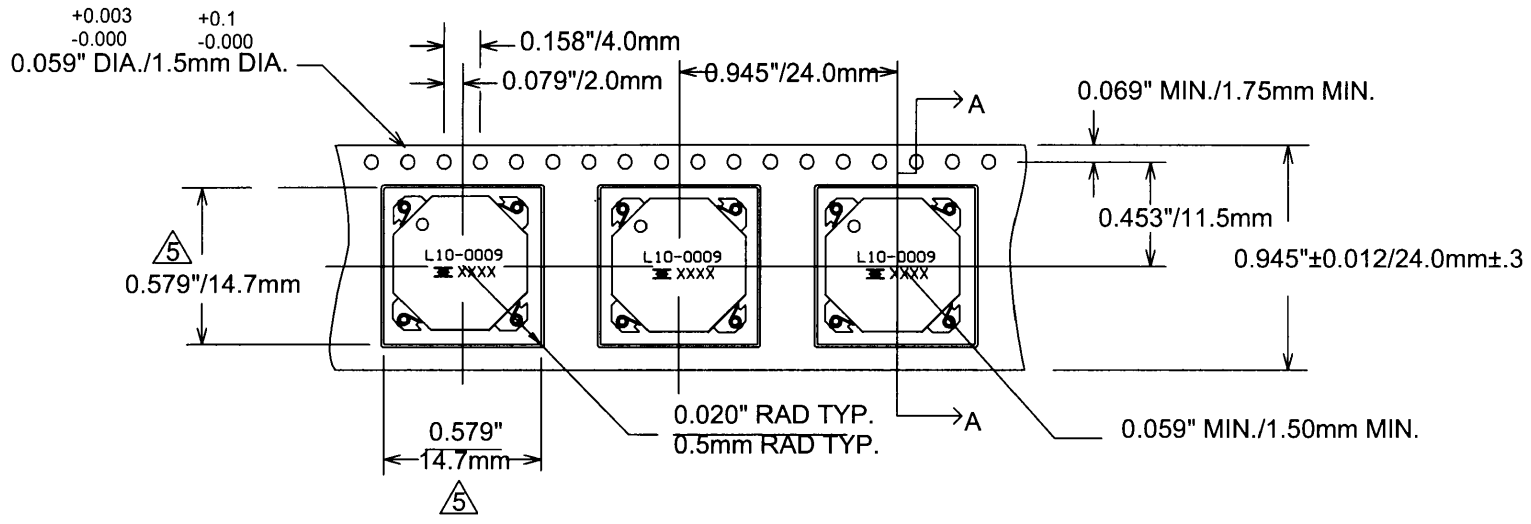
INTERWINDING CAPACITANCE: 55pF NOM. (1-2) 120 KHz 50mV

DC RESISTANCE: 1.125 OHMS NOM. (1-4) (2-3)

IR: 9K MEGOHMS MIN. @ 200 VDC (1-2)


RoHS Compliant

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY		
				DRAWN:	K. LOFTUS	© 1998 BH Electronics
				ENGR:	B. GASS	TITLE:
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:
						TITLE:
						PART NO.
						L10-0009
ISSUE	DATE	REVISION	BY	PAGE 1 OF 2		



CARRIER TAPE 811-0137

SECTION A-A

				DESIGN PER:	BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics	
				<input type="checkbox"/> CUSTOM	<input checked="" type="checkbox"/> NON-PROPRIETARY		
				DRAWN:	K. LOFTUS		
				ENGR:	J. DECRAMER		
C	1-28-08	SHOW BRANDED		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005		TITLE:	CHOKE
B	9-13-05	SHOW NEW CASE	JDC			PART NO.	L10-0009
A	3-16-05	CUSTOMER PRINT (Q11516-2)	JDC				
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2			