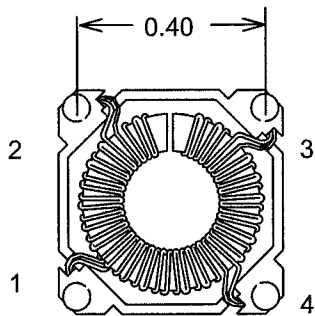
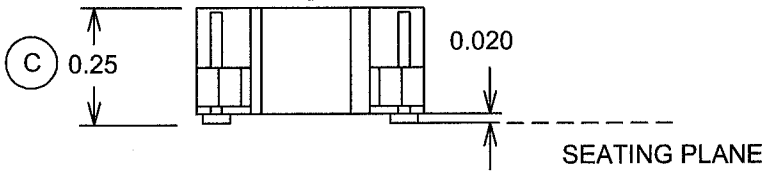
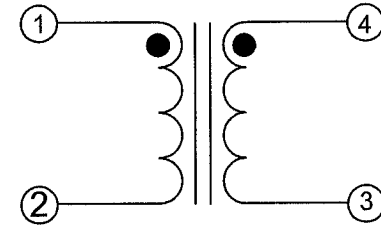
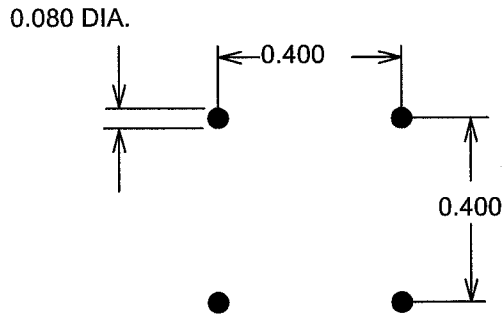
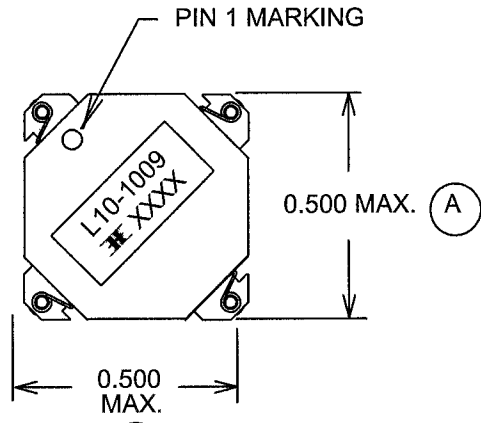


MECHANICAL

RECOMMENDED PC BOARD LAYOUT

SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1



OPEN CIRCUIT INDUCTANCE: 4.56uH±20% @ 0 ADC (1-2) 10 KHz 100mV

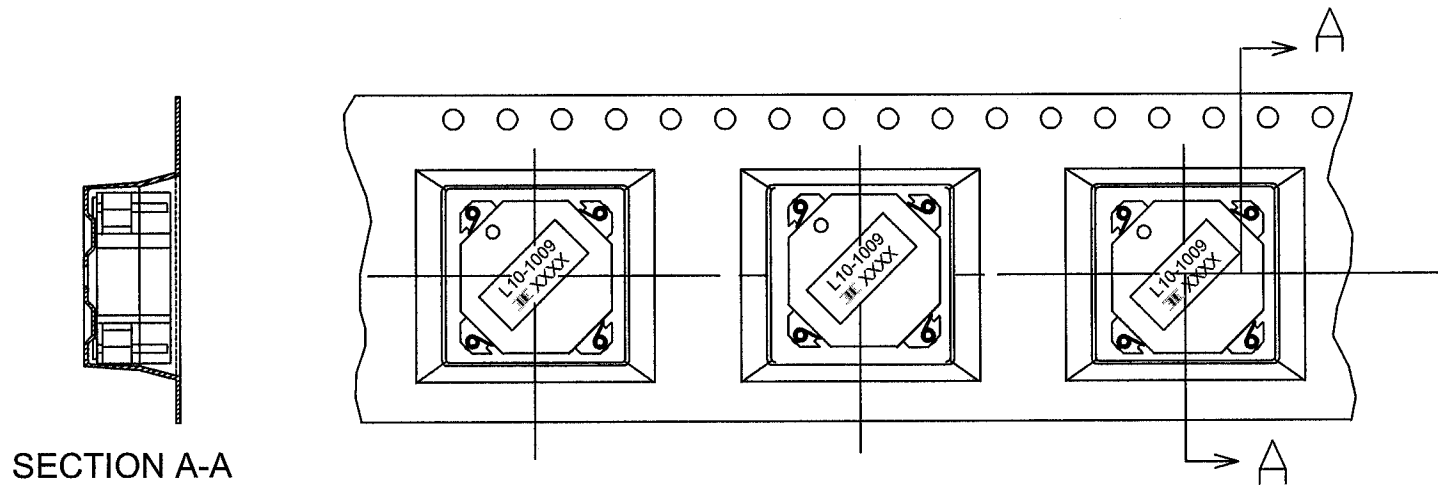
OPEN CIRCUIT INDUCTANCE: 3.65uH TYP. @ 6.47 ADC (1-2) 10 KHz 100mV

DC RESISTANCE: (1-2) (4-3) 0.0185 OHMS NOM.


IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS Compliant

				DESIGNED PER: BH CATALOG	 (R) BH Electronics, Inc. Burnsville & Marshall, MN © 1998 BH Electronics
				CUSTOM NON-PROPRIETARY <input checked="" type="checkbox"/>	
				DRAWN Kathy Loftus	TITLE: QTR PAK™ (Inductor, Switching Regulator)
				ENGR. J. DECRAMER	
B	9-13-05	ADD RoHS COMPLIANT	 JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005	PART NO. L10-1009
A	2-22-05	CUSTOMER PRINT			
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CARRIER TAPE 811-0137

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				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN Kathy Loftus	
				ENGR. J. DECRAMER	TITLE: QTR PAK™ (Inductor, Switching Regulator)
B	9-13-05	ADD RoHS COMPLIANT		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005	PART NO. L10-1009
A	2-22-05	CUSTOMER PRINT	JDC		
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				PAGE 2 OF 2	