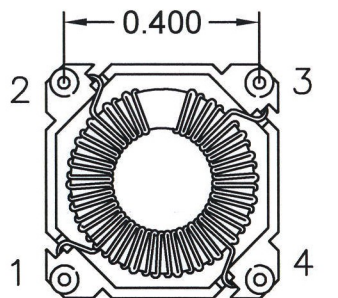
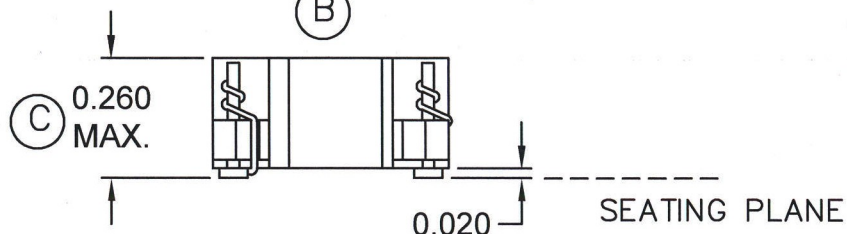
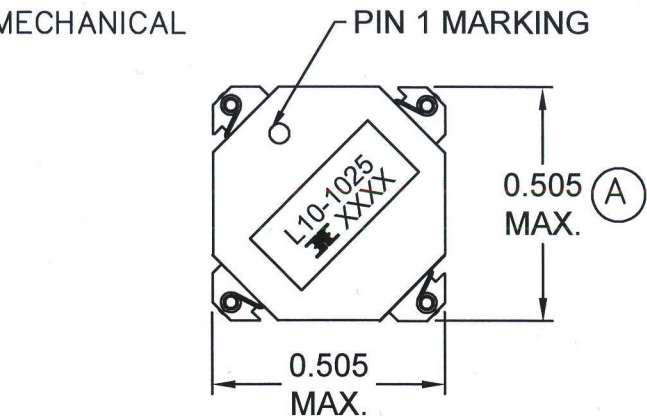
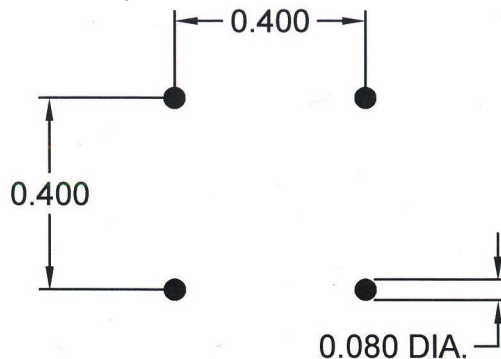


MECHANICAL

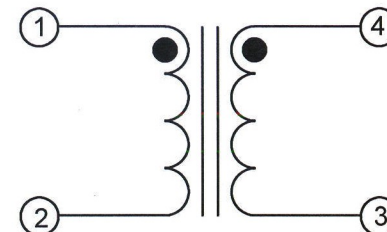


COPLANARITY 0.004"

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 34.86 μ H - 52.30 μ H @ 0 ADC (1,4-2,3)

10 KHz 100mV

DC BIAS: 34.86 μ H TYP. @ 2.09 ADC (1, 4 - 2, 3)

10 KHz 100mV


DC RESISTANCE: (1-2)(4-3) 0.19 OHMS NOM.

IR: 100 VDC (1-4) 1K MEGOHMS MIN.

PRIMARY LEAKAGE INDUCTANCE: 330nH NOM. (1-2 SH 3,4)

120 KHz 50 mV

RoHS Compliant

				DESIGNED PER:		 (R) BH Electronics, Inc. Burnsville & Marshall, MN (C) 1998 BH Electronics
				BH CATALOG		
				CUSTOM	NON-PROPRIETARY	X
				DRAWN Kathy Loftus		
				ENGR. J. DECRAMER		
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX \pm .01 .XXX \pm .005		
				PAGE 1 OF 2		
				TITLE: TM		
				PART NO. INDUCTOR		
				L10-1025		
ISSUE	DATE	REVISION	BY			
C	2-27-19	UPDATE ELECTRICALS	B.G.			
B	9-13-05	ADD RoHS COMPLIANT	JDC			
A	5-19-05	CUSTOMER PRINT	JDC			

