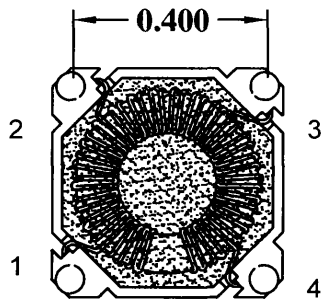
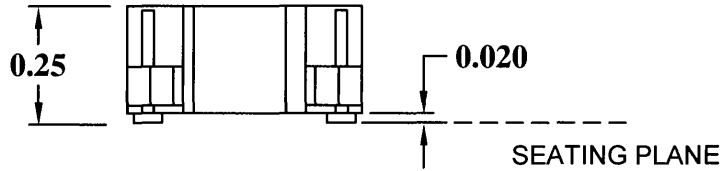
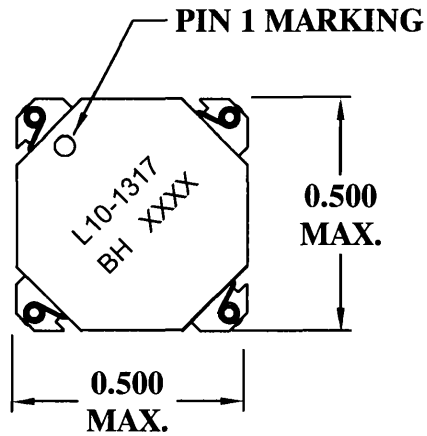
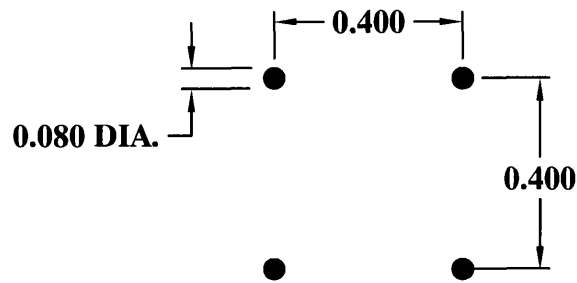


MECHANICAL

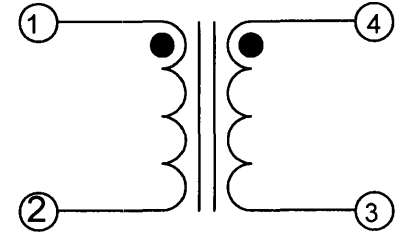


COPLANARITY 0.004"

RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 13.6 μ H @ 0 ADC 10 KHz 100mV (1-2)


DC BIAS: 10.9 μ H TYP. @ 4.20 ADC 10 KHz 100mV (1-2)

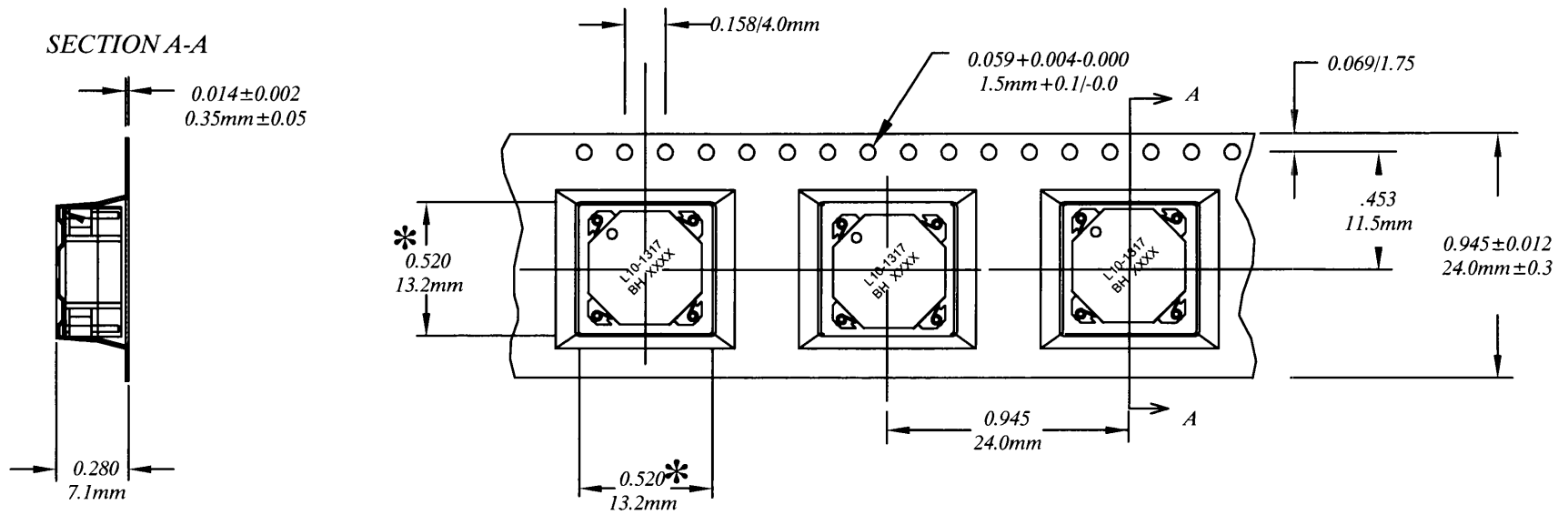
PRIMARY LEAKAGE INDUCTANCE: 0.25 μ H NOM.

DC RESISTANCE: 0.182 OHMS NOM. (1-2) (4-3)


HIGH VOLTAGE TEST: 3000 VAC / 1 SEC. WITH 5 SEC. RAMP-UP (1-4)

RoHS Compliant

				DESIGNED PER: BH CATALOG	 ® BH Electronics, Inc. Burnsville & Marshall, MN © 1998 BH Electronics
				CUSTOM NON-PROPRIETARY <input checked="" type="checkbox"/>	
				DRAWN Kathy Loftus	TITLE: QTR PAK™ (Switching Regulator)
				ENGR. J. DECRAMER	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE.	PART NO. L10-1317
				.XX±.01 .XXX±.005	
A	2-5-13	CUSTOMER PRINT	<i>gll</i>	PAGE 1 OF 2	
ISSUE	DATE	REVISION	BY		



CARRIER TAPE 811-0137

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				BH CATALOG <input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN Kathy Loftus	© 1998 BH Electronics
				ENGR. J. DECRAMER	TITLE: QTR PAK™ (Switching Regulator)
A	2-5-13	CUSTOMER PRINT		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE. .XX±.01 .XXX±.005	PART NO. L10-1317
ISSUE	DATE	REVISION	BY	PAGE 2 OF 2	