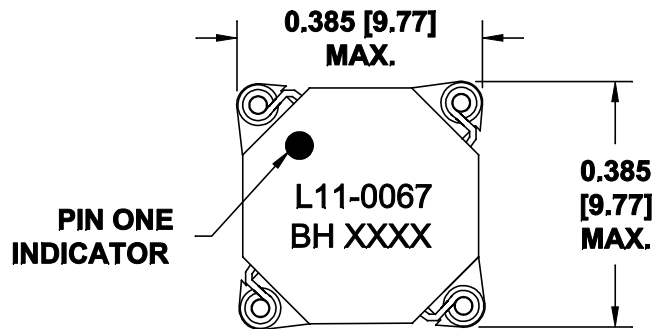
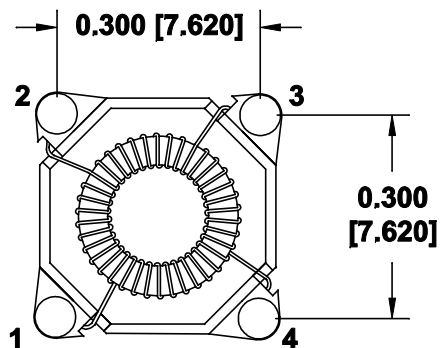
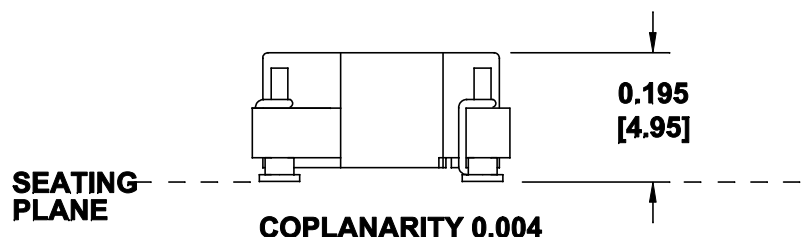


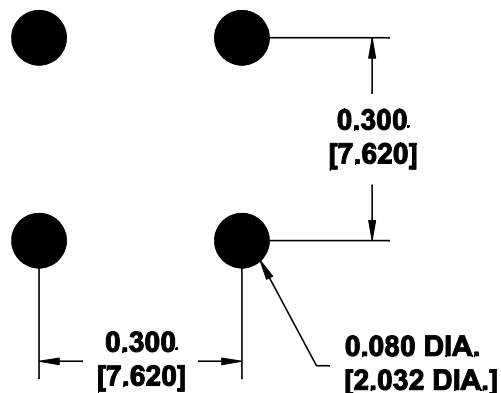
MECHANICAL



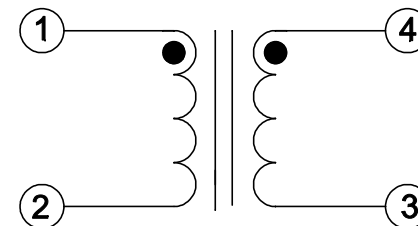
NOTE: LABEL MUST NOT EXTEND OVER EDGES OF CASE



RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 4:1


OPEN CIRCUIT INDUCTANCE: 223μH +/- 20%. 10 KHz 100mV
 178μH NOM. @ 0.41 ADC (1-2)

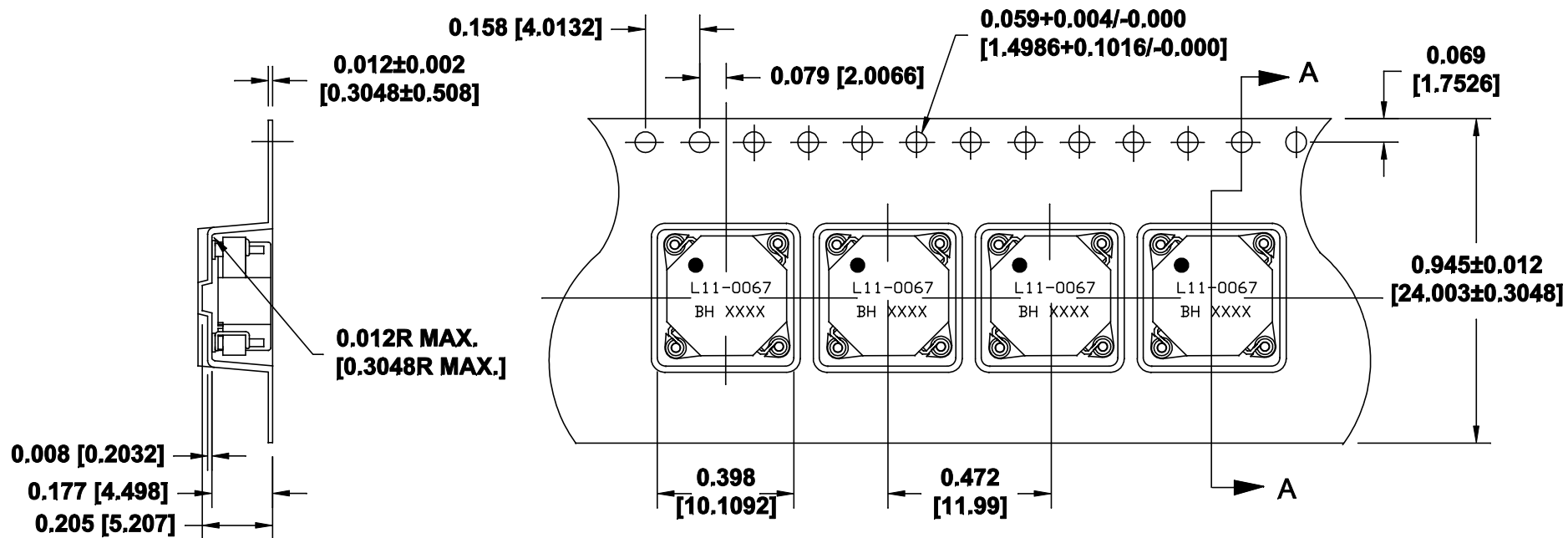
PRIMARY LEAKAGE INDUCTANCE: 2.16μH NOM. (1-2 SH 3, 4)
 120 KHz 50mV

DC RESISTANCE: 1.7 OHMS MAX. (1-2) 0.170 OHMS MAX. (3-4)


HIGH VOLTAGE TEST: 1500 VAC (1-4)

RoHS COMPLIANT

				DESIGN PER:		 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG		
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY		
D	8-8-13	REVISE DCR AND DIMS.	JDC	DRAWN: K. LOFTUS	TITLE:	
C	8-9-11	Q12786-1 NOW L11-0067	JDC	ENGR: J. DECRAMER	1/8 PAK	
B	4-12-11	ADD LOC SPEC.	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO.	
A	4-8-11	CUSTOMER PRINT	JDC		L11-0067	
ISSUE	DATE	REVISION	BY		PAGE 1 OF 2	



CARRIER TAPE 811-0187

				DESIGN PER:	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				BH CATALOG	
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
D	8-8-13	REVISE DCR AND DIMS	JDC	DRAWN: K. LOFTUS	
C	8-9-11	Q12786-1 NOW L11-0067	JDC	ENGR: J. DECRAMER	
B	4-12-11	ADD LOC SPEC.	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	
A	4-8-11	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY		
				PAGE 2 OF 2	TITLE: 1/8 PAK PART NO. L11-0067