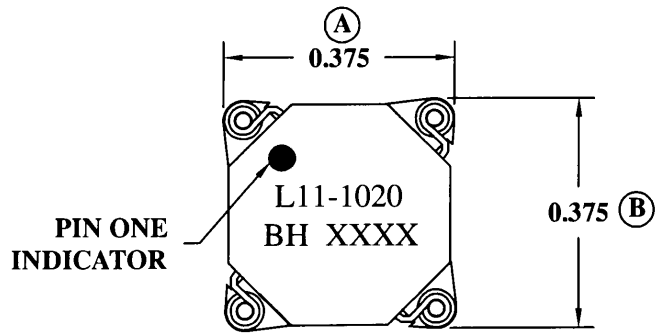
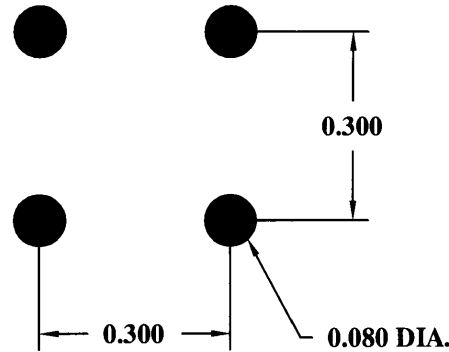


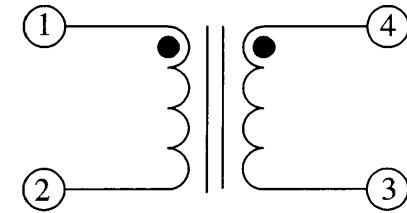
MECHANICAL



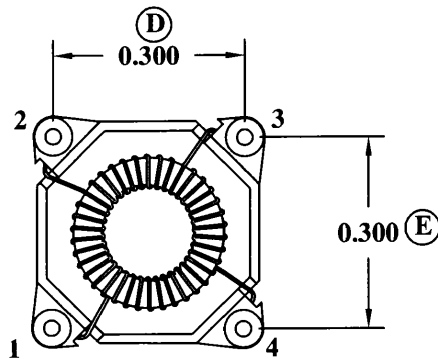
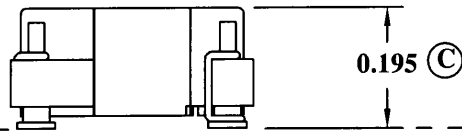
RECOMMENDED PC BOARD LAYOUT



SCHEMATIC



SEATING PLANE



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1


OPEN CIRCUIT INDUCTANCE: 33.53-50.29 μ H @ 0 ADC (1-2) 10 KHz 100mV

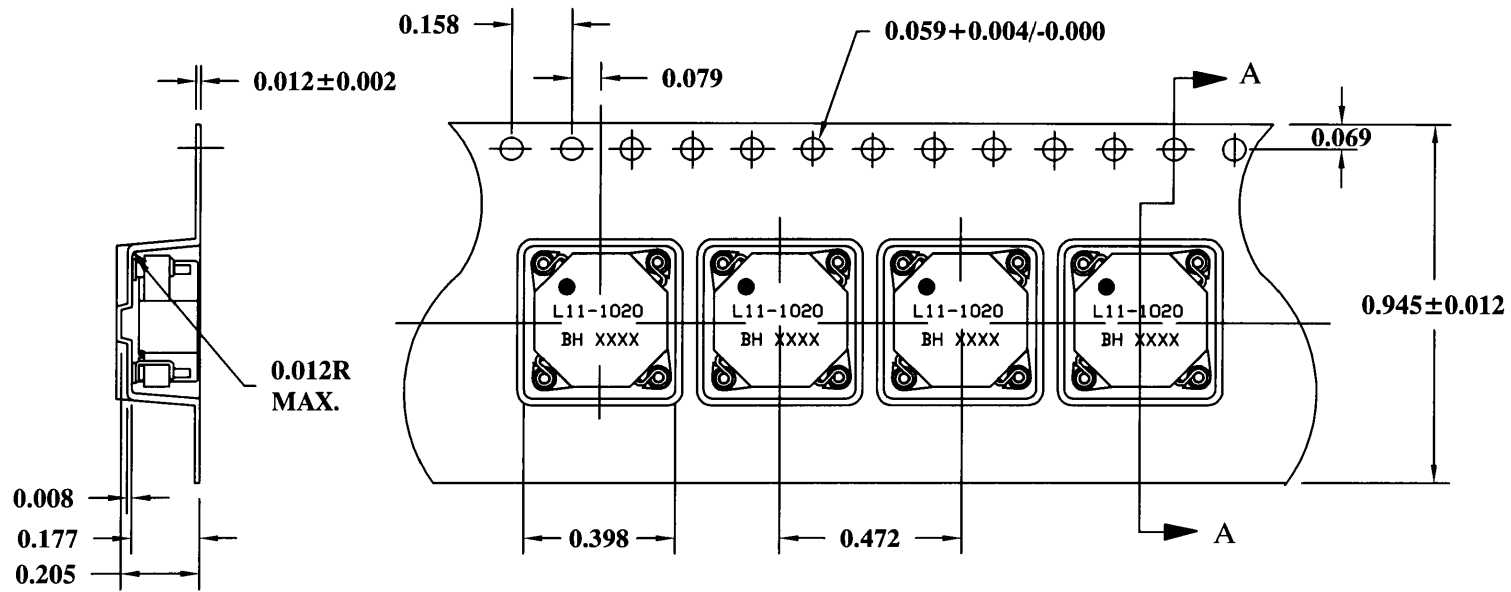
DC BIAS: 33.53 μ H NOM. @ 0.94 ADC (1-2)
10 KHz 100mV

DC RESISTANCE: 0.2972 OHMS NOM. (1-2) (3-4)


IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS Compliant

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				DRAWN: K. LOFTUS	TITLE: INDUCTOR
				ENGR: J. DECRAMER	
				UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX \pm .01 .XXX \pm .005	PART NO. L11-1020
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ISSUE	DATE	REVISION	BY		