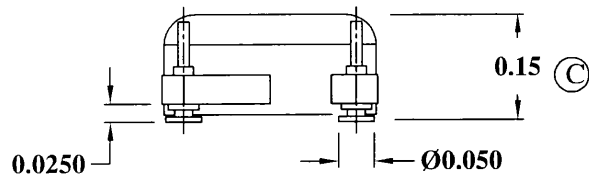
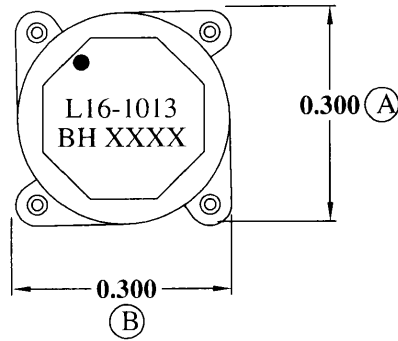
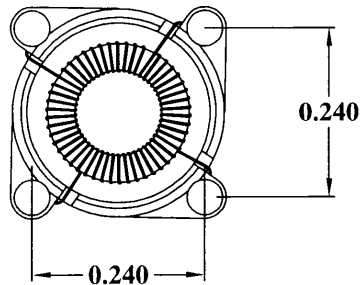


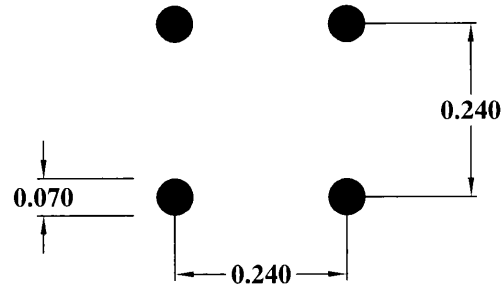
MECHANICAL



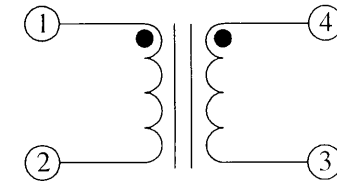
COPLANARITY 0.004 MAX.



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1



OPEN CIRCUIT INDUCTANCE: 8.35µH±20% @ 0 ADC (1-2) (SH 1, 4) (SH 2, 3)
10 KHz 100mV

DC BIAS: 6.68µH TYP. @ 2.68 ADC (1-2) (SH 1, 4) (SH 2, 3)
10 KHz 100mV

DC RESISTANCE: 0.094 OHMS MAX. (1-2) (SH 1, 4) (SH 2, 3)

IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

				DESIGN PER: BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM <input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN: K. LOFTUS	TITLE: INDUCTOR
				ENGR: J. DECRAMER	
C	11-23-11	REVISE DIMENSIONS		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005	PART NO. L16-1013
B	9-25-07	REVISE DIMENSIONS	JDC		
A	8-16-07	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY	PAGE 1 OF 1	