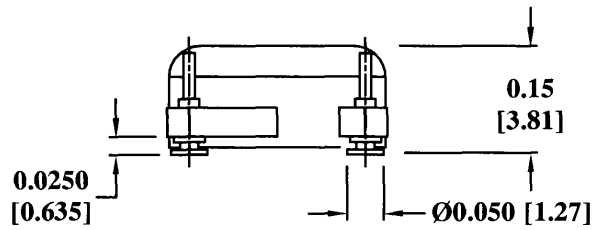
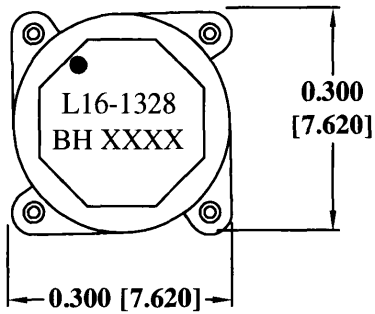
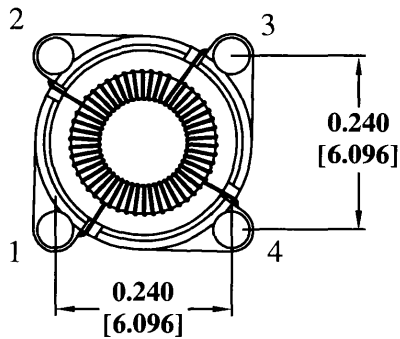


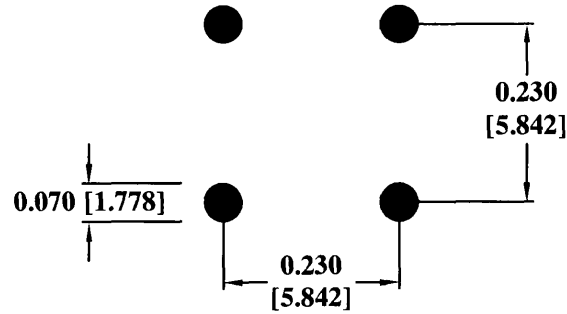
MECHANICAL



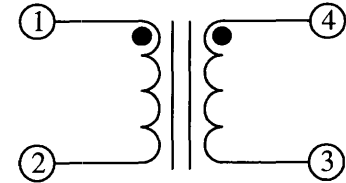
COPLANARITY 0.004 MAX.



SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 42.3 $\mu$ H $\pm$ 20% @ 0 ADC (1, 4 - 2, 3)  
10 KHz 100mV



DC BIAS: 33.8 $\mu$ H TYP. @ 1.19 ADC (1, 4 - 2, 3)

PRIMARY LEAKAGE INDUCTANCE: 0.4 $\mu$ H NOM.  
10 KHz 100mV

DC RESISTANCE: 1.54 OHMS NOM. (1, 4 - 2, 3)

HIGH VOLTAGE TEST: 1500 VAC (1-4)

RoHS COMPLIANT

				DESIGN PER:	CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM	<input checked="" type="checkbox"/> NON-PROPRIETARY	
				DRAWN:	K. LOFTUS	TITLE: <b>INDUCTOR</b>
				ENGR:	J. DECRAMER	
A	2-5-13	CUSTOMER PRINT		UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX $\pm$ .01 .XXX $\pm$ .005		PART NO. <b>L16-1328</b>
ISSUE	DATE	REVISION	BY			